

FIG. 1  
PRIOR ART

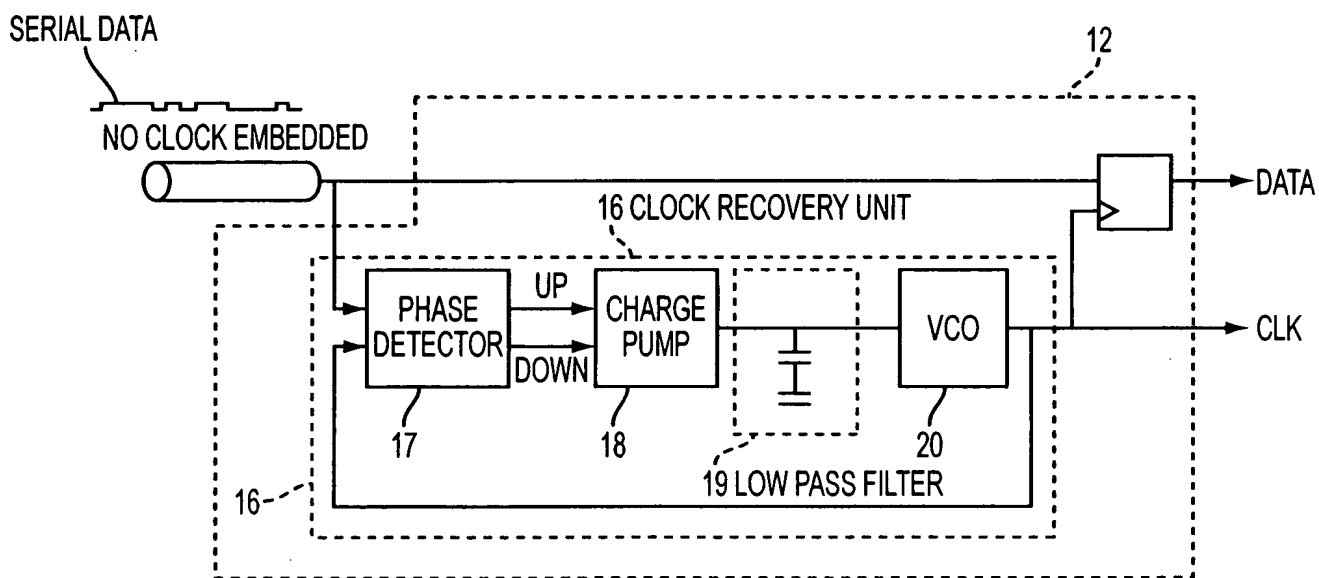


FIG. 2  
PRIOR ART

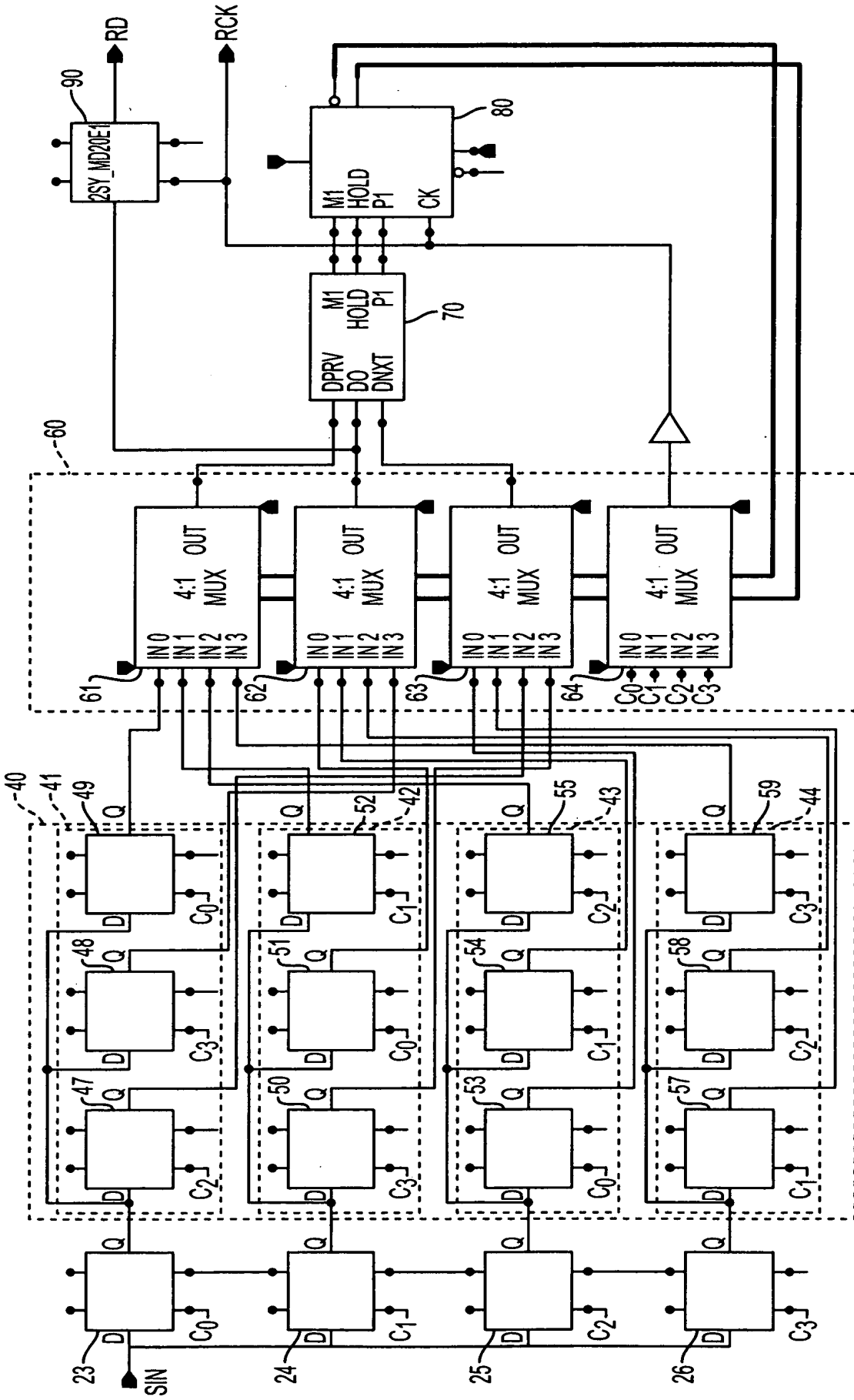


FIG. 3A

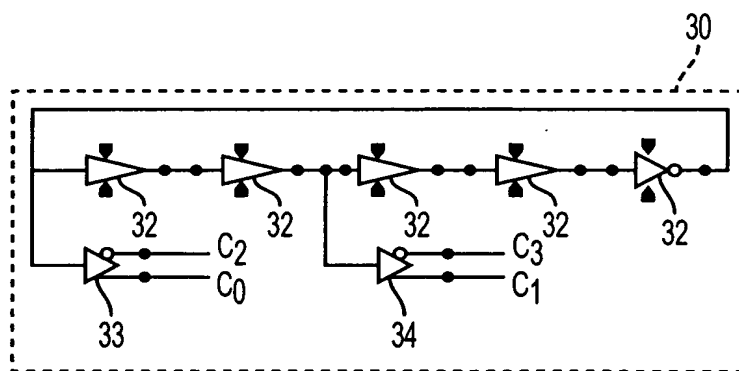


FIG. 3B

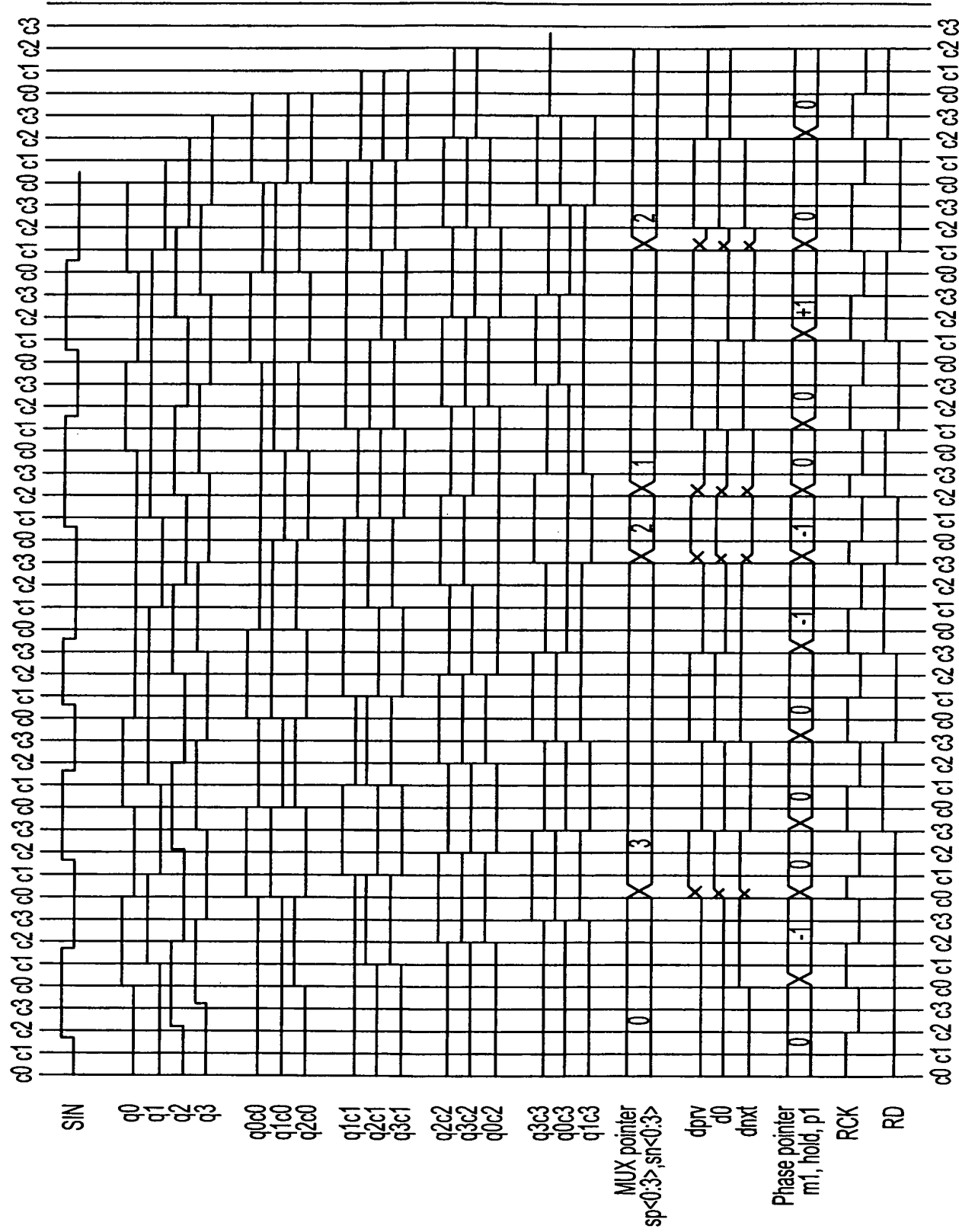


FIG. 4

DPRV	D0	DNXT	M1	HOLD	P1
H	H	L	H	L	L
L	L	H	H	L	L
H	H	H	L	H	L
L	L	L	L	H	L
H	L	L	L	L	H
L	H	H	L	L	H

FIG. 5

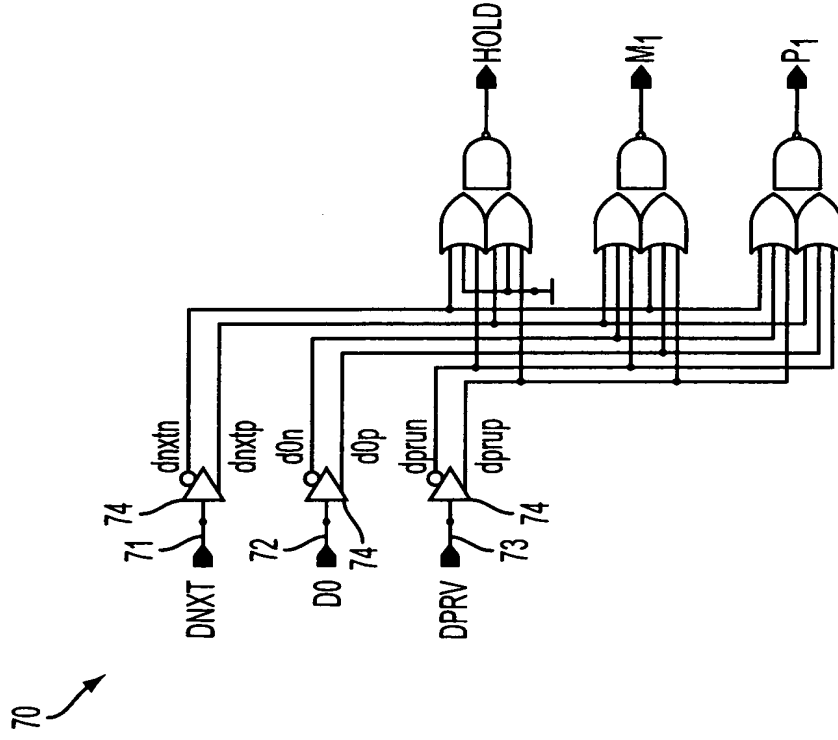


FIG. 6

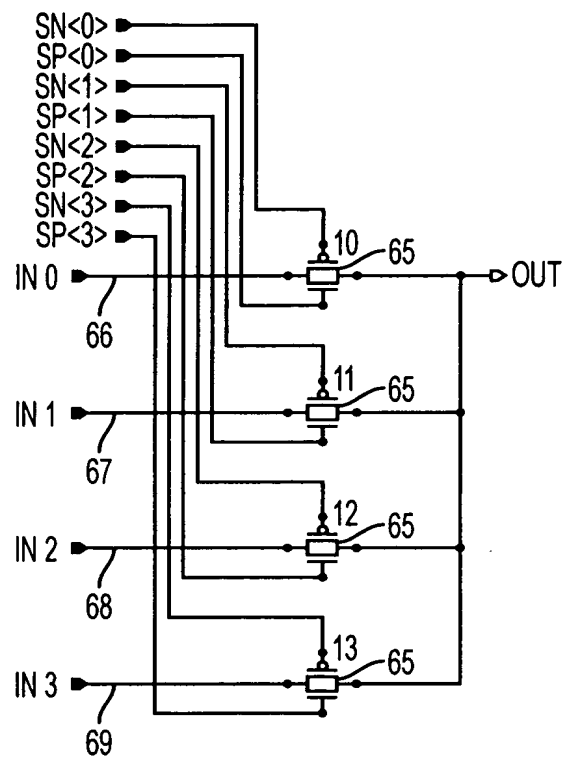


FIG. 7

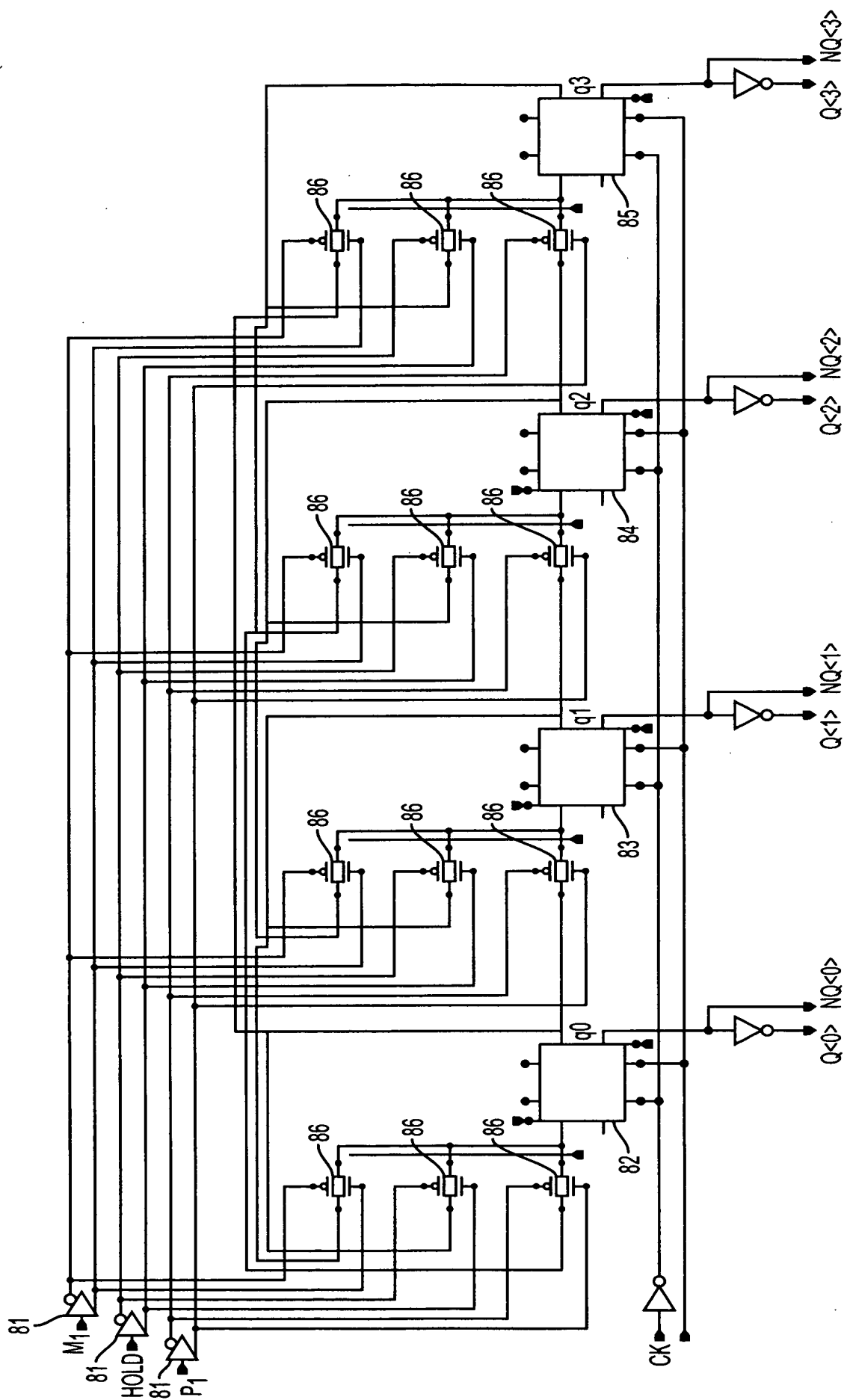


FIG. 8

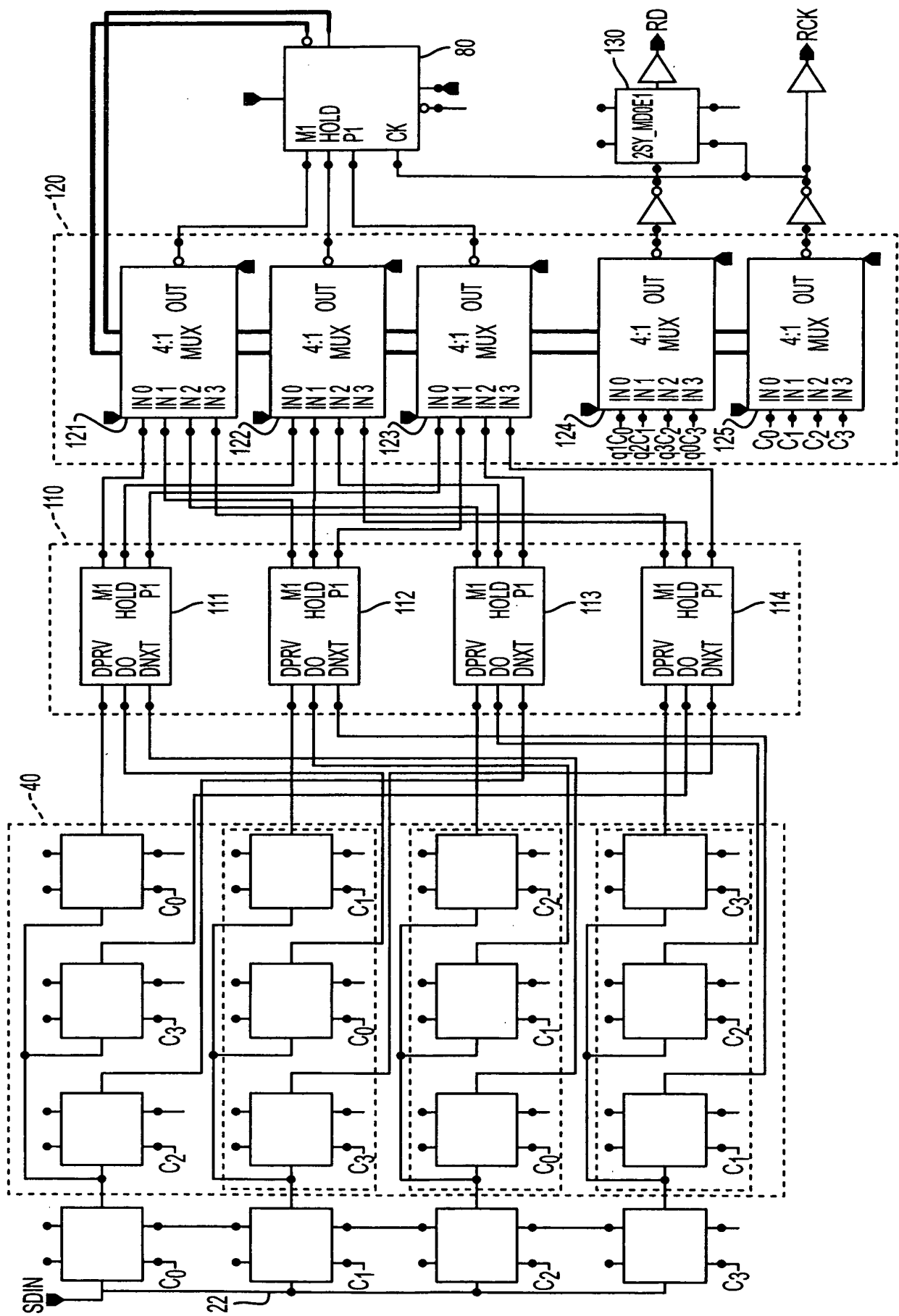


FIG. 9A



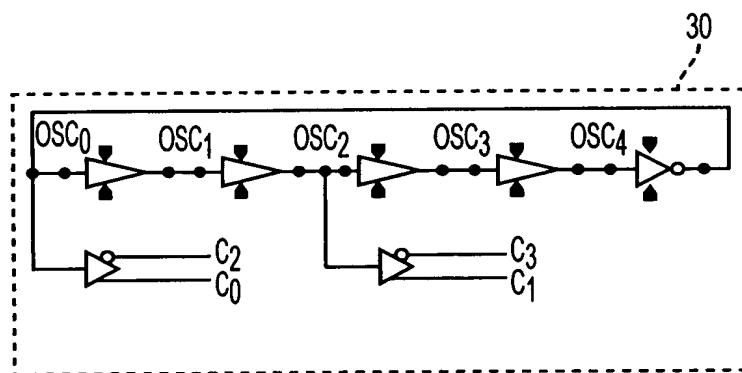


FIG. 9B

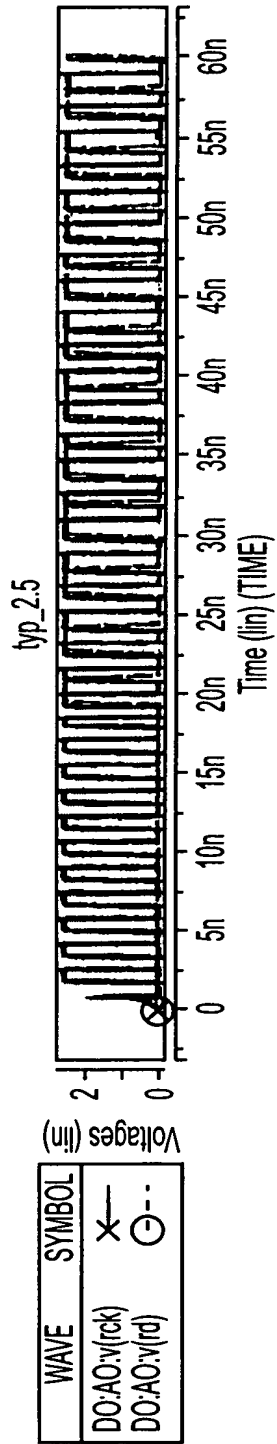


FIG. 10A

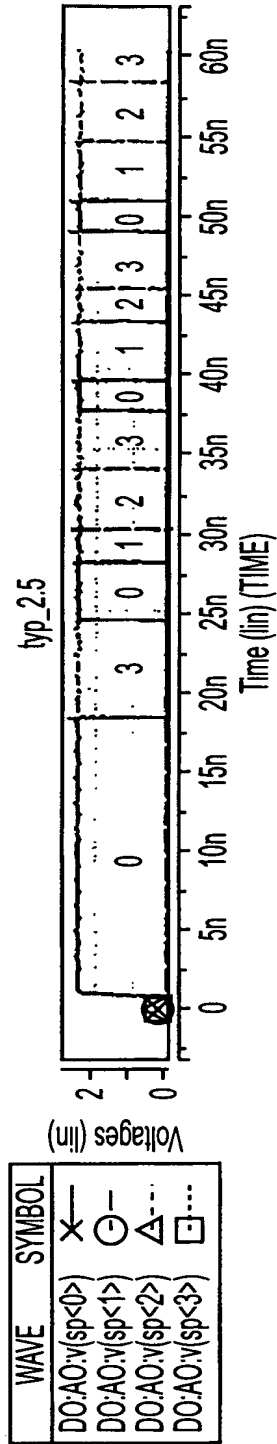


FIG. 10B

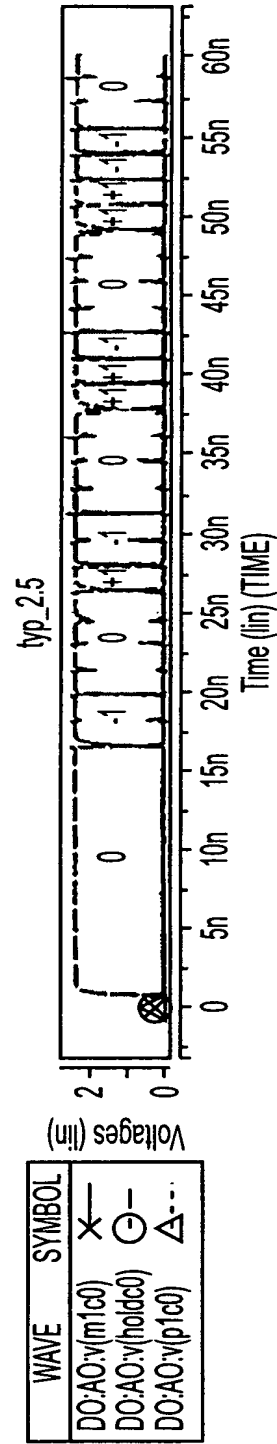
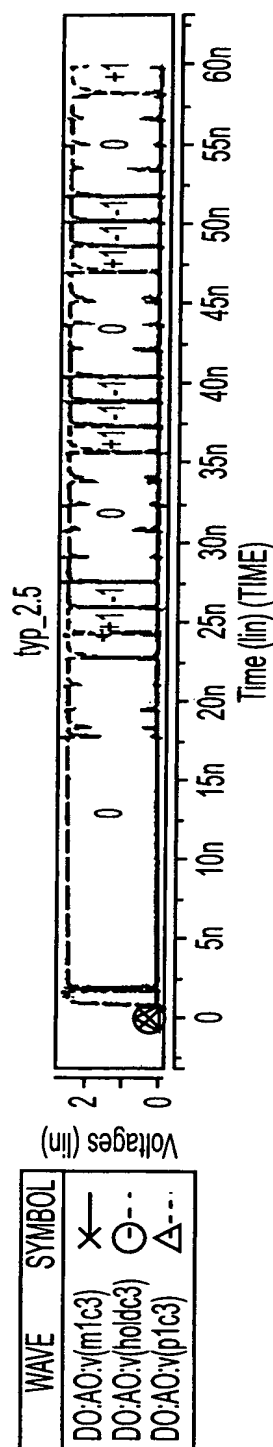
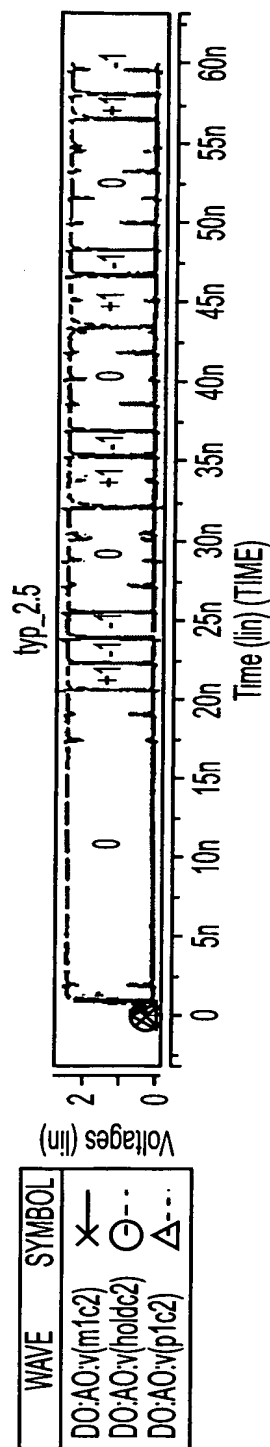
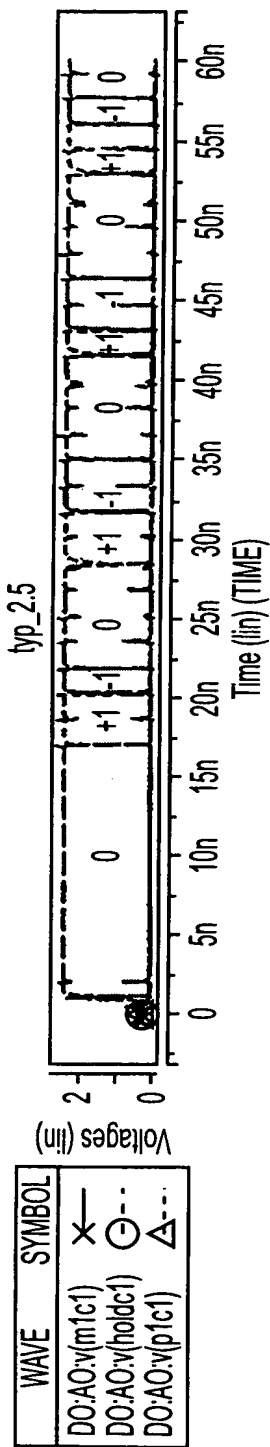
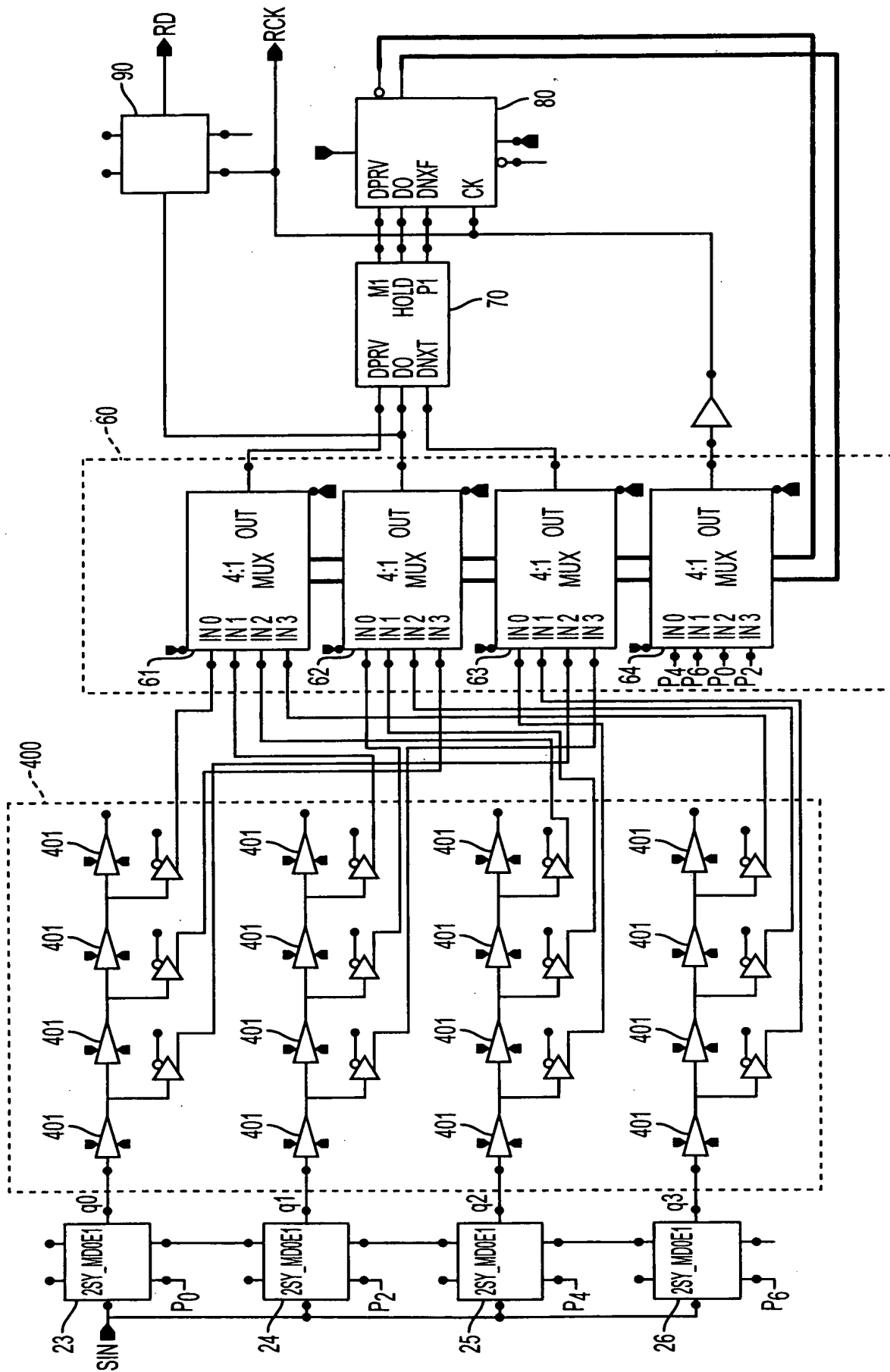


FIG. 10C





**FIG. 11A**

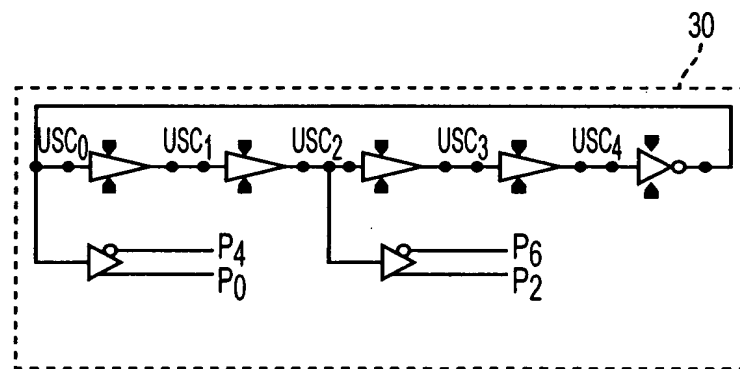


FIG. 11B

typ\_2.5

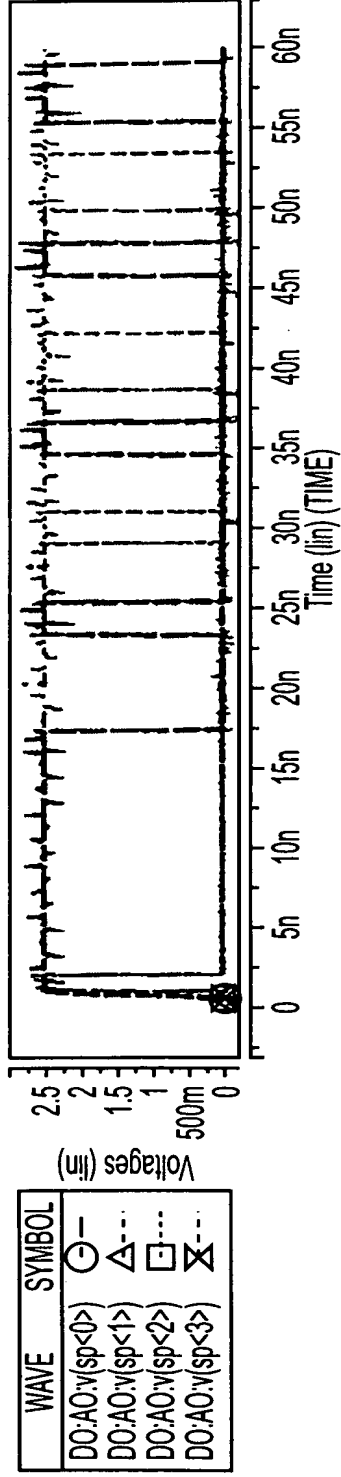


FIG. 12A

typ\_2.5

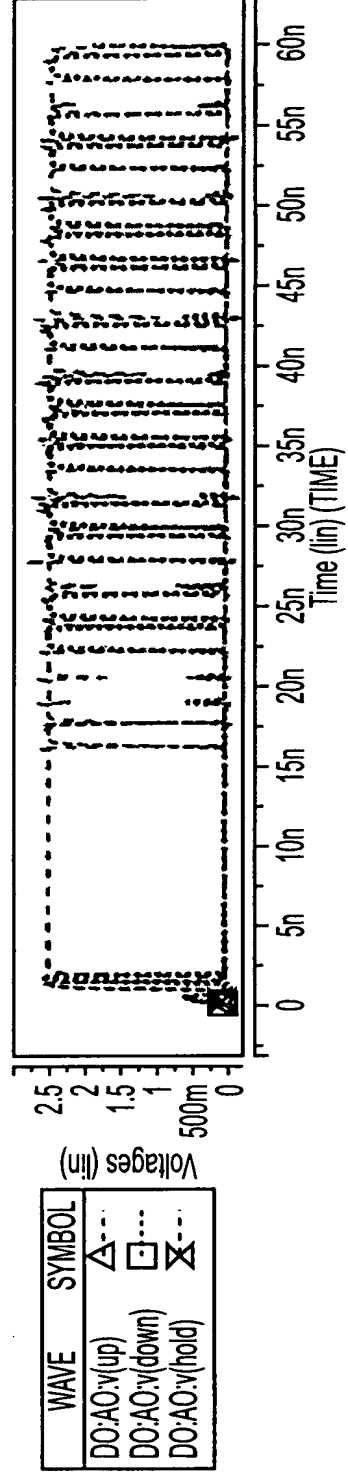


FIG. 12B

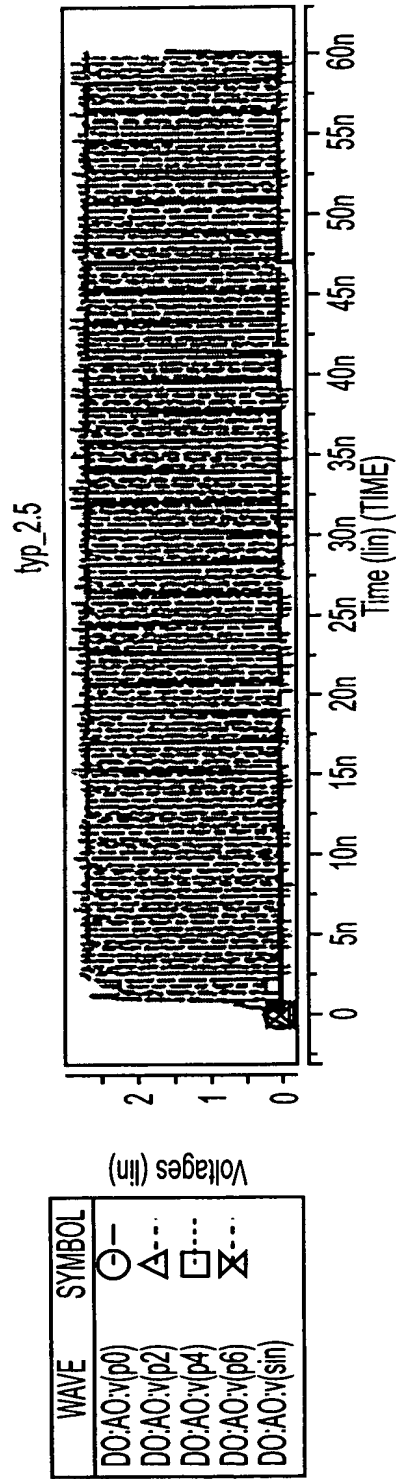


FIG. 12C

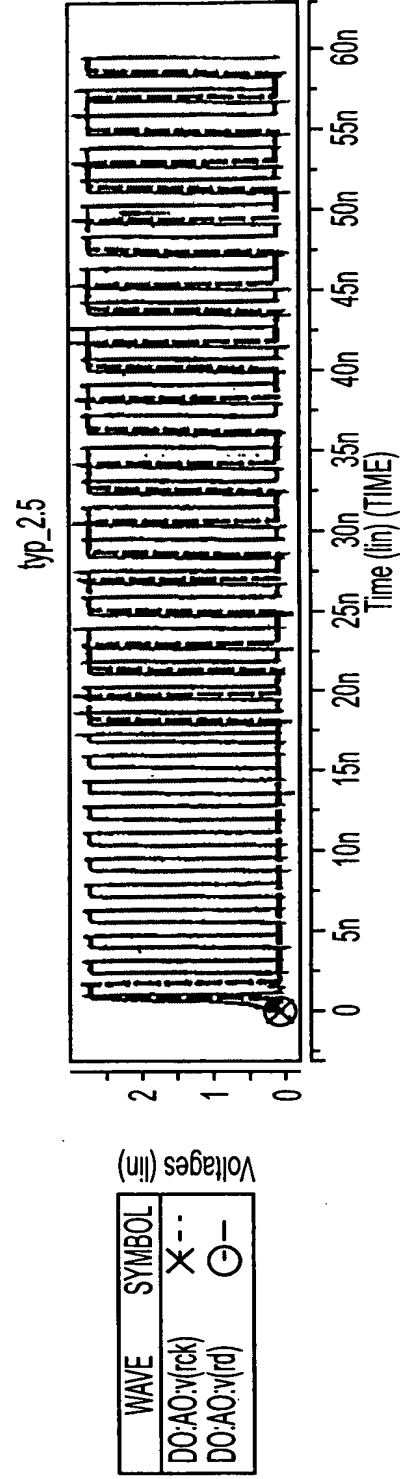


FIG. 12D

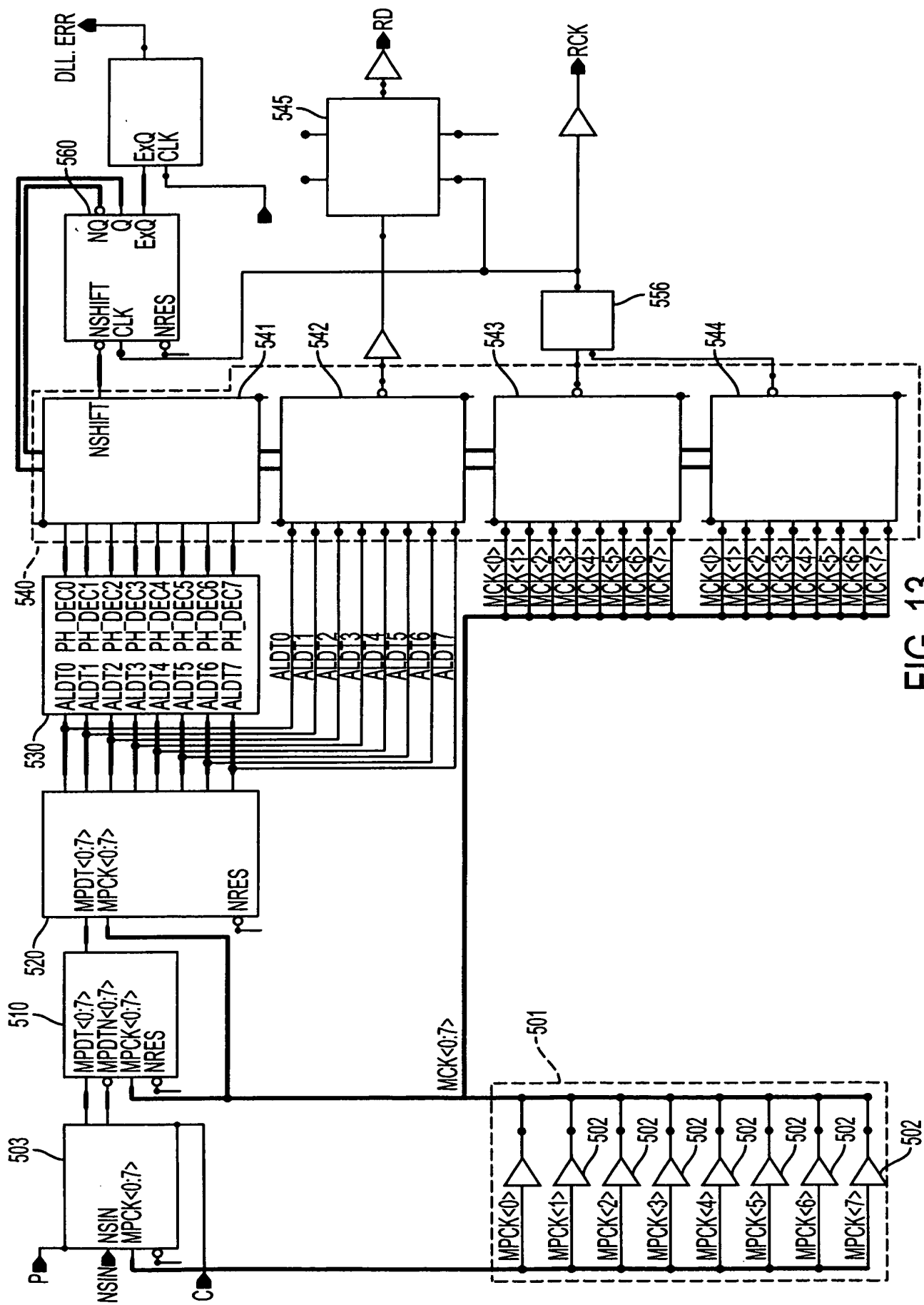


FIG. 13



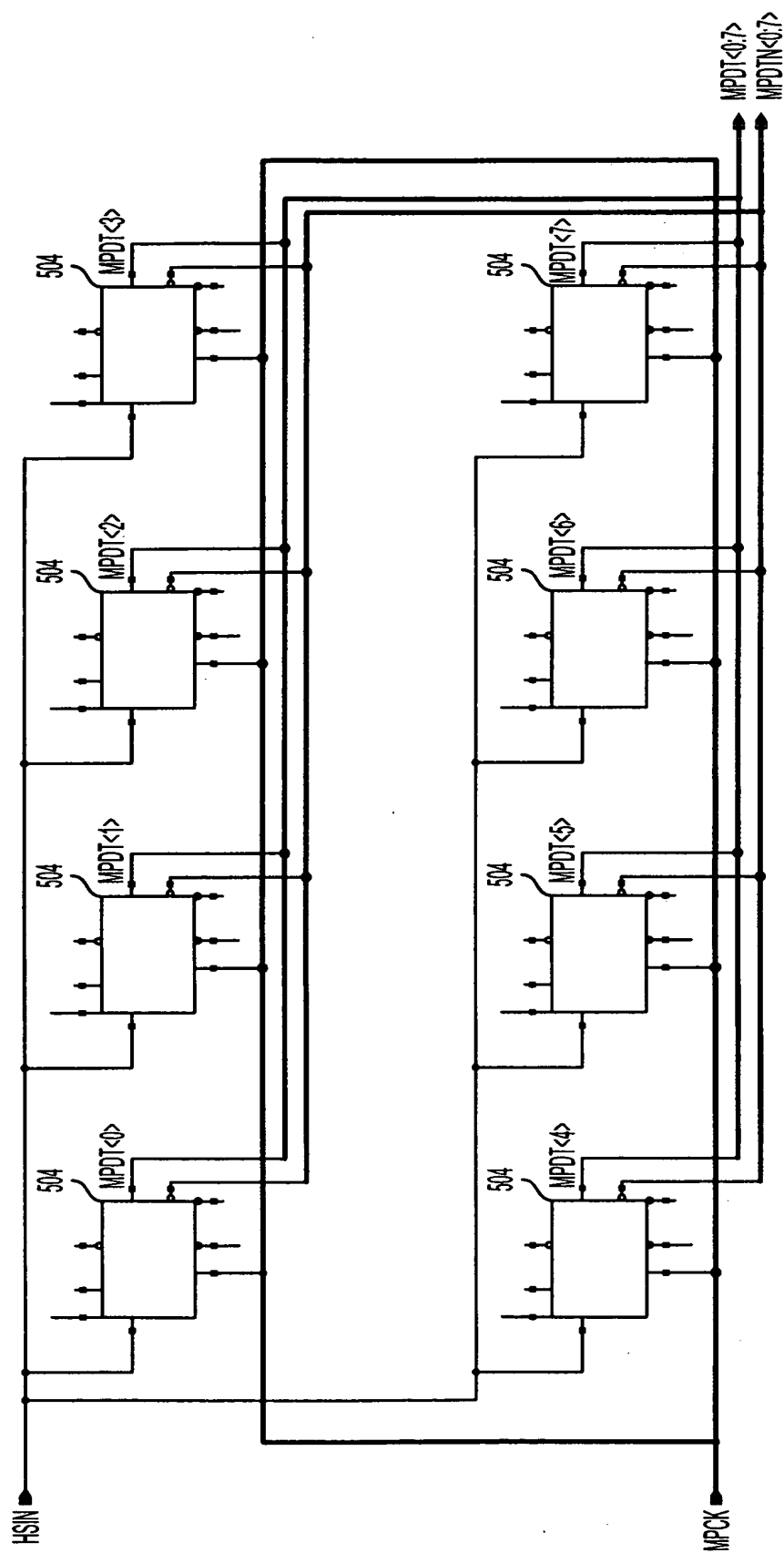


FIG. 14

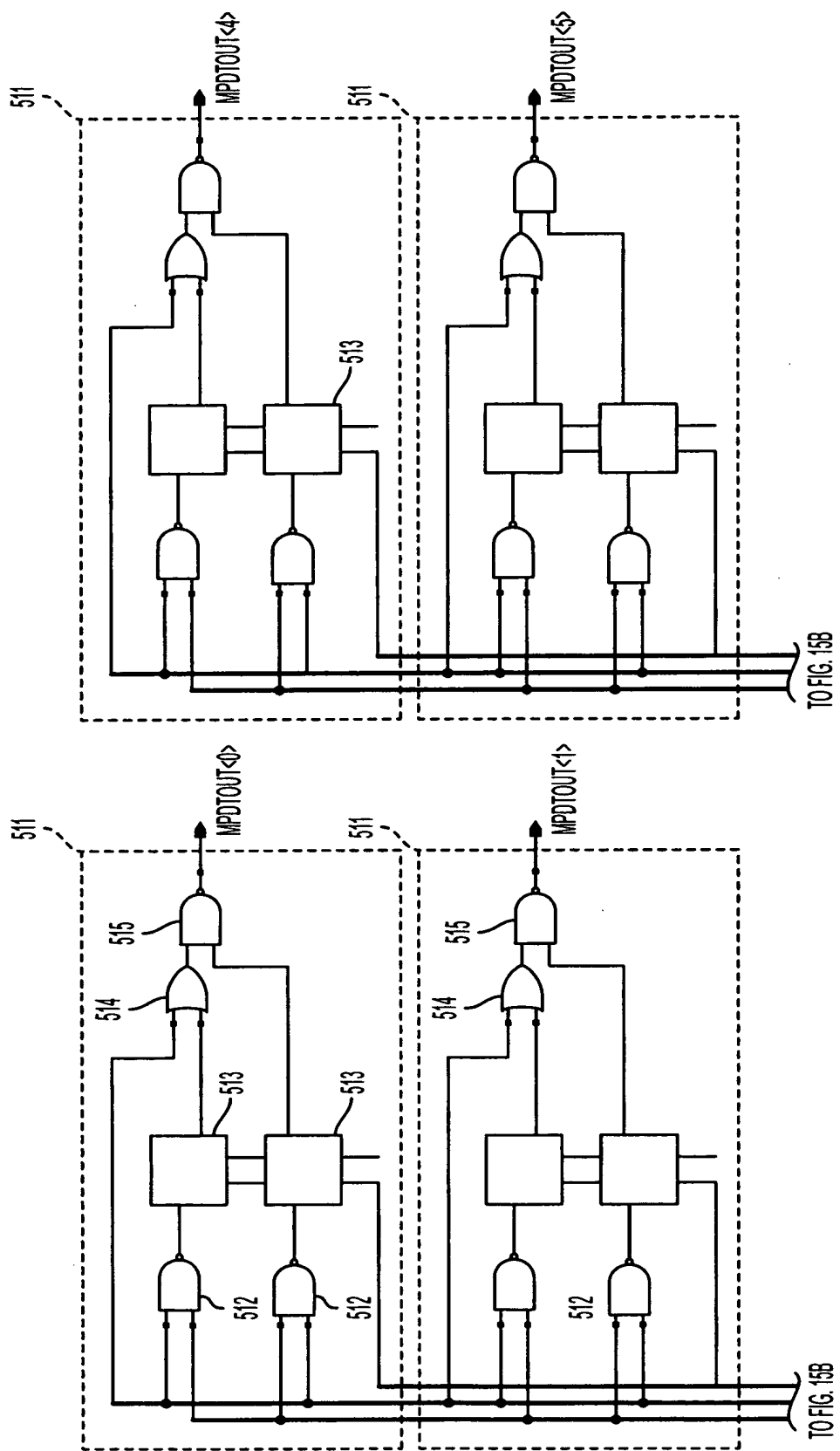


FIG. 15A



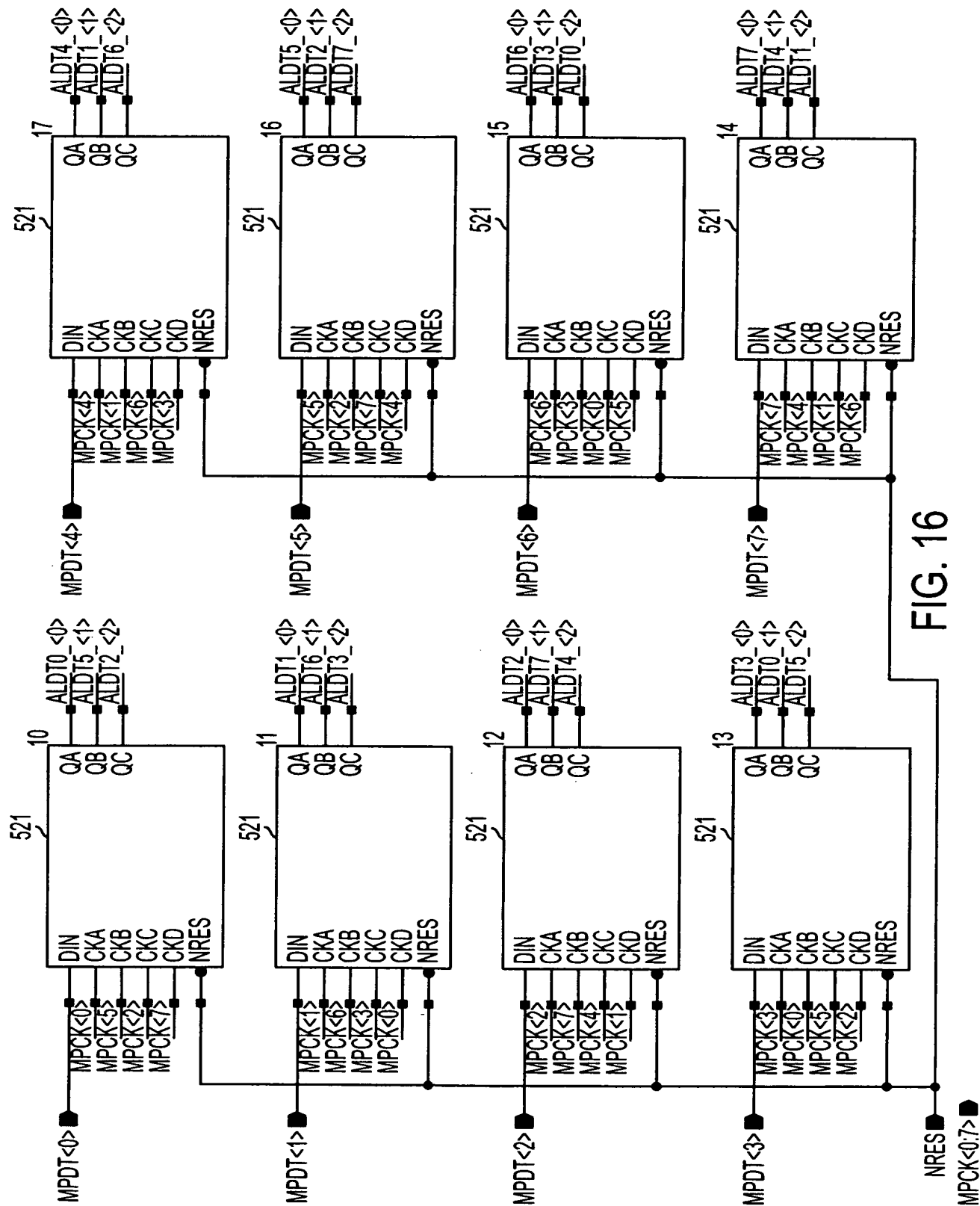


FIG. 16

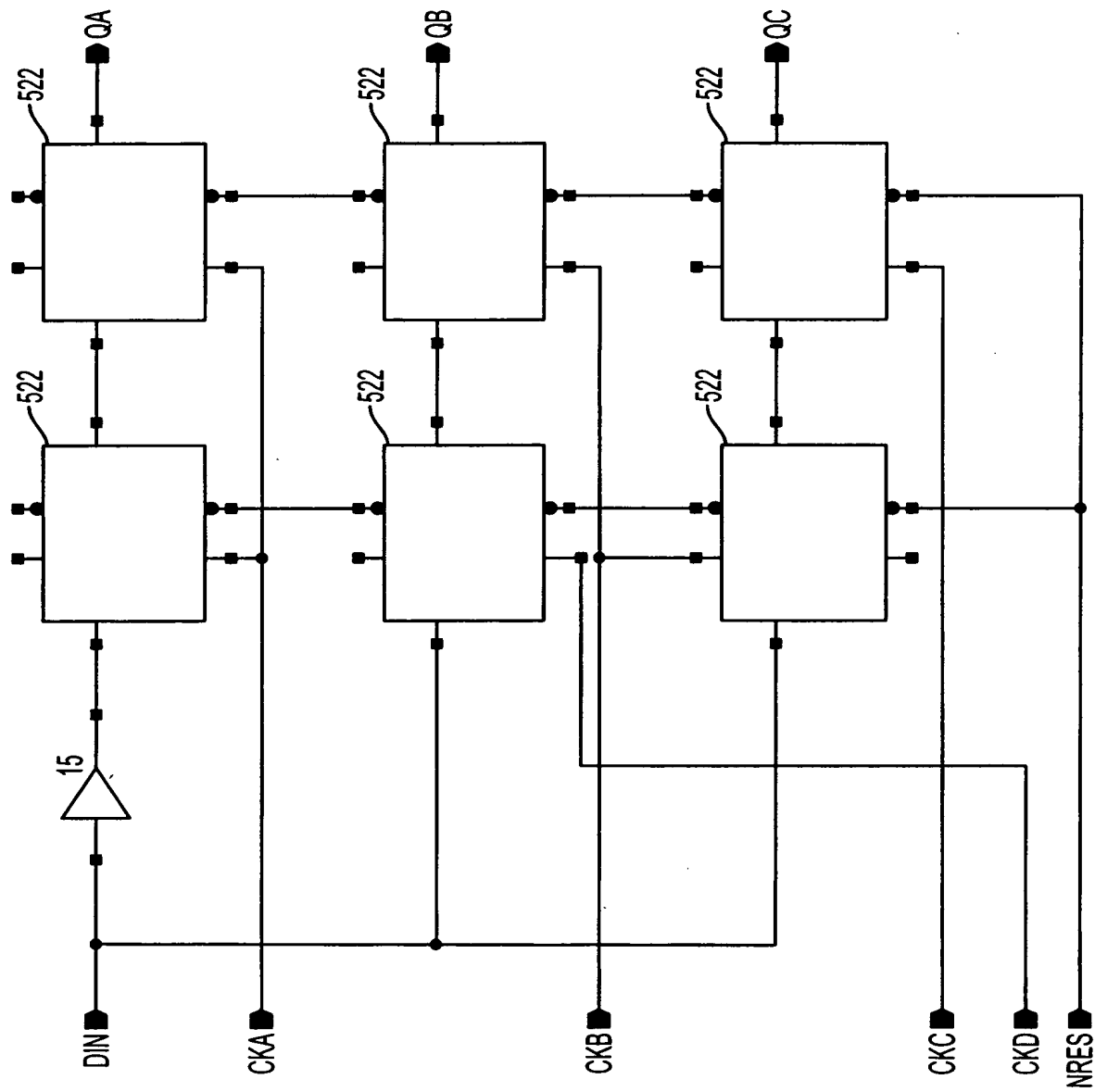


FIG. 17

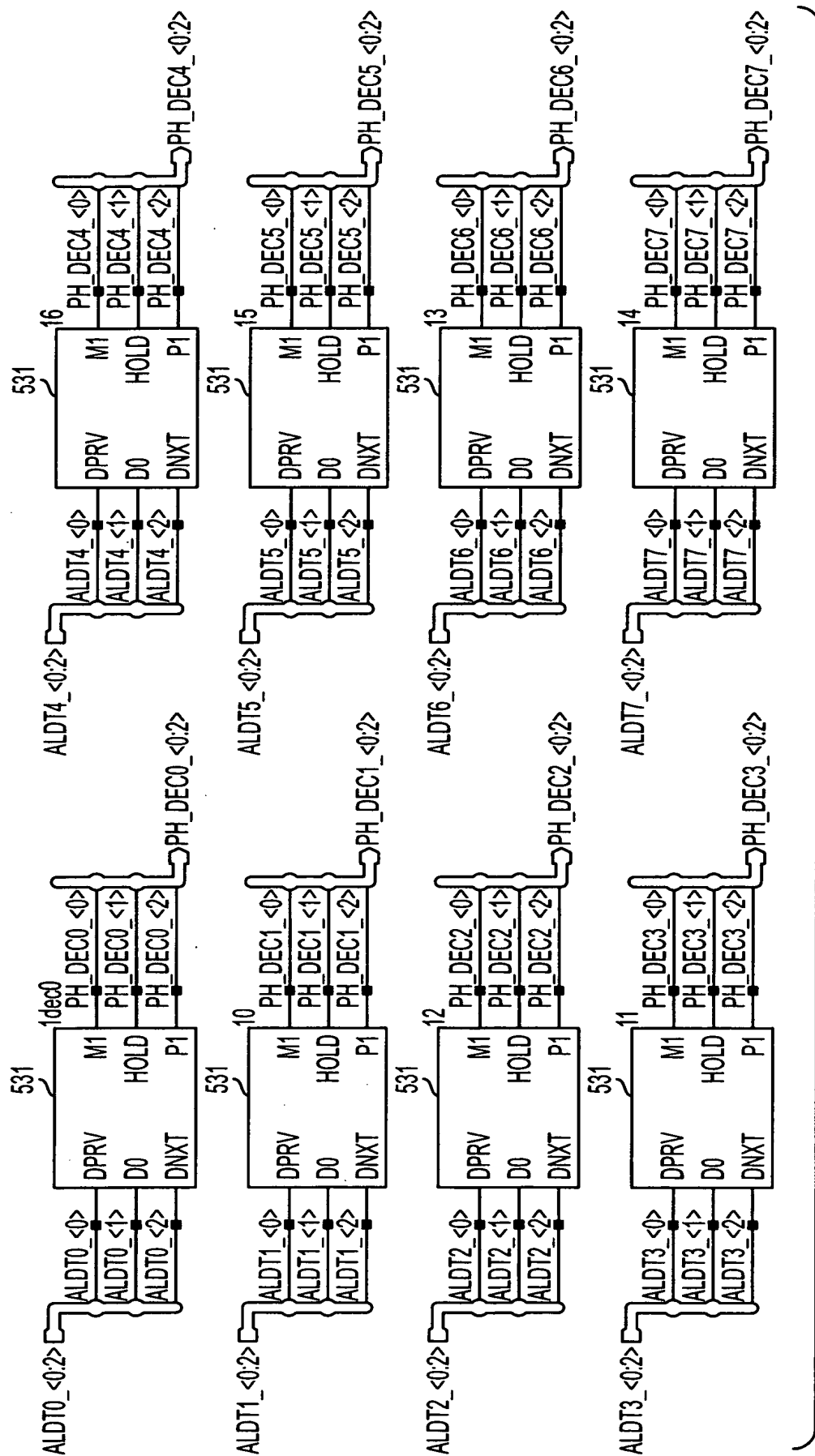


FIG. 18

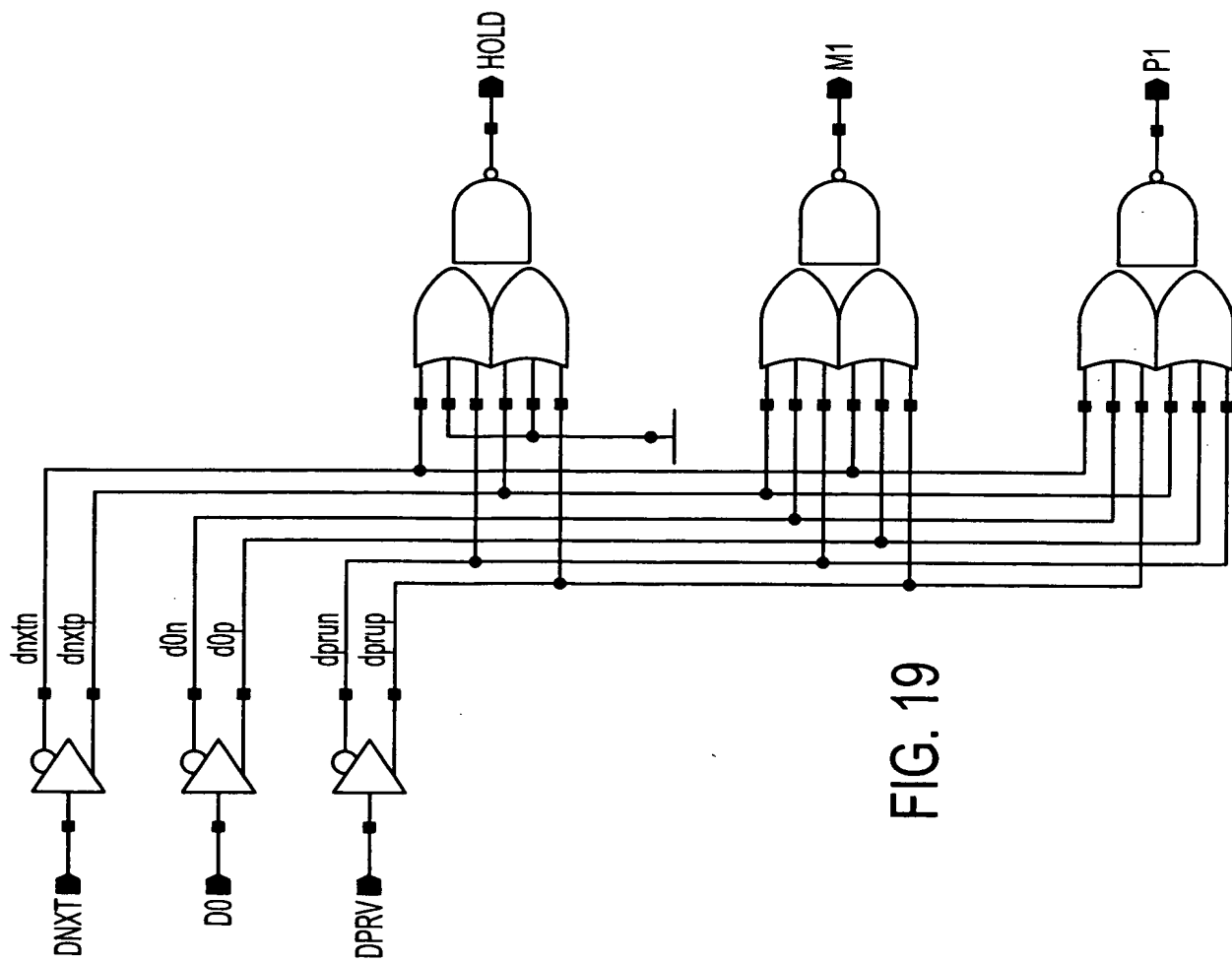


FIG. 19

DPRV	D0	DNXT	M1	HOLD	P1
H	L	L	H	L	L
L	L	H	H	L	L
H	H	H	L	H	L
L	L	L	L	H	L
H	L	L	L	L	H
L	H	H	L	L	H

FIG. 20

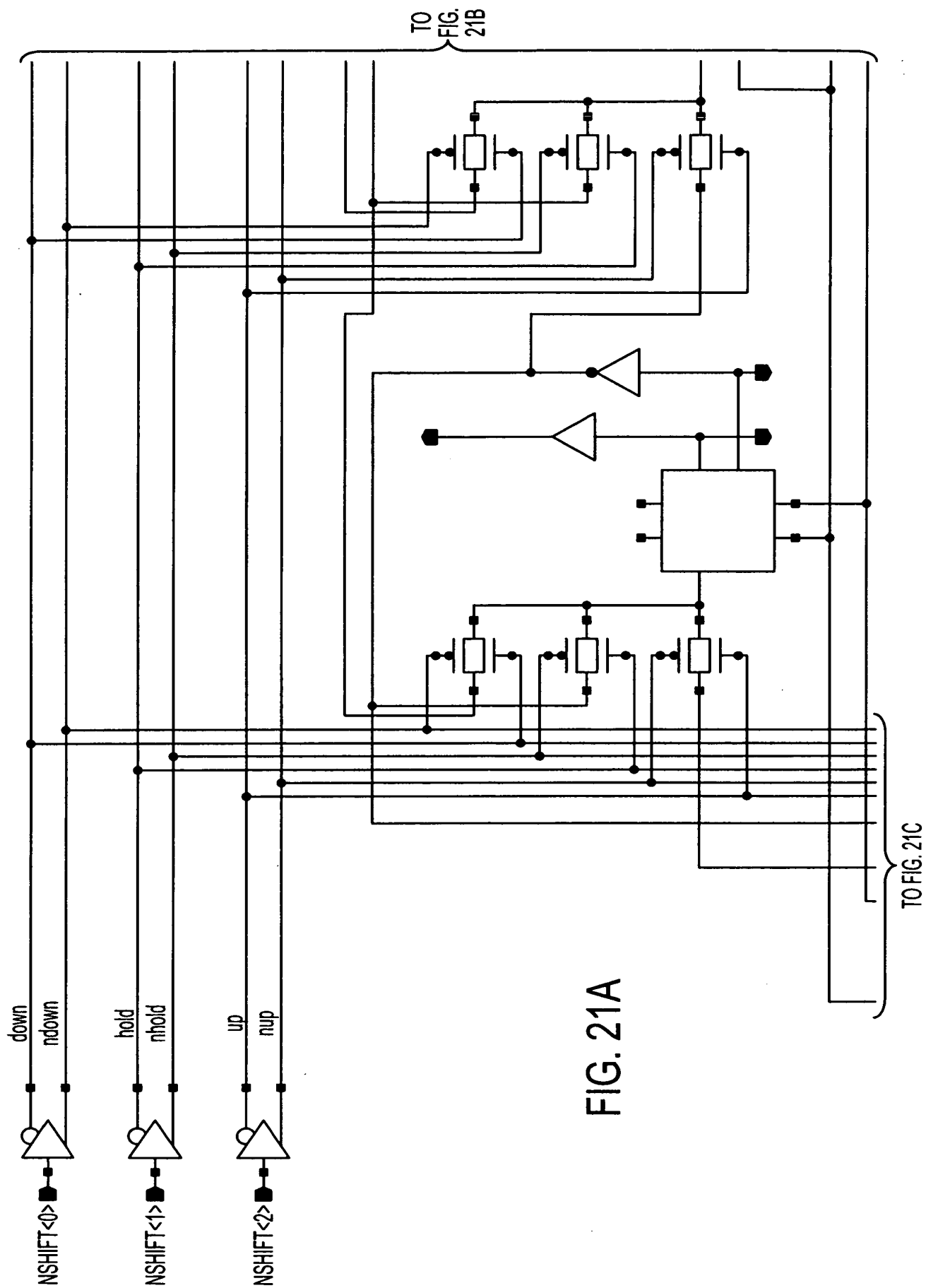
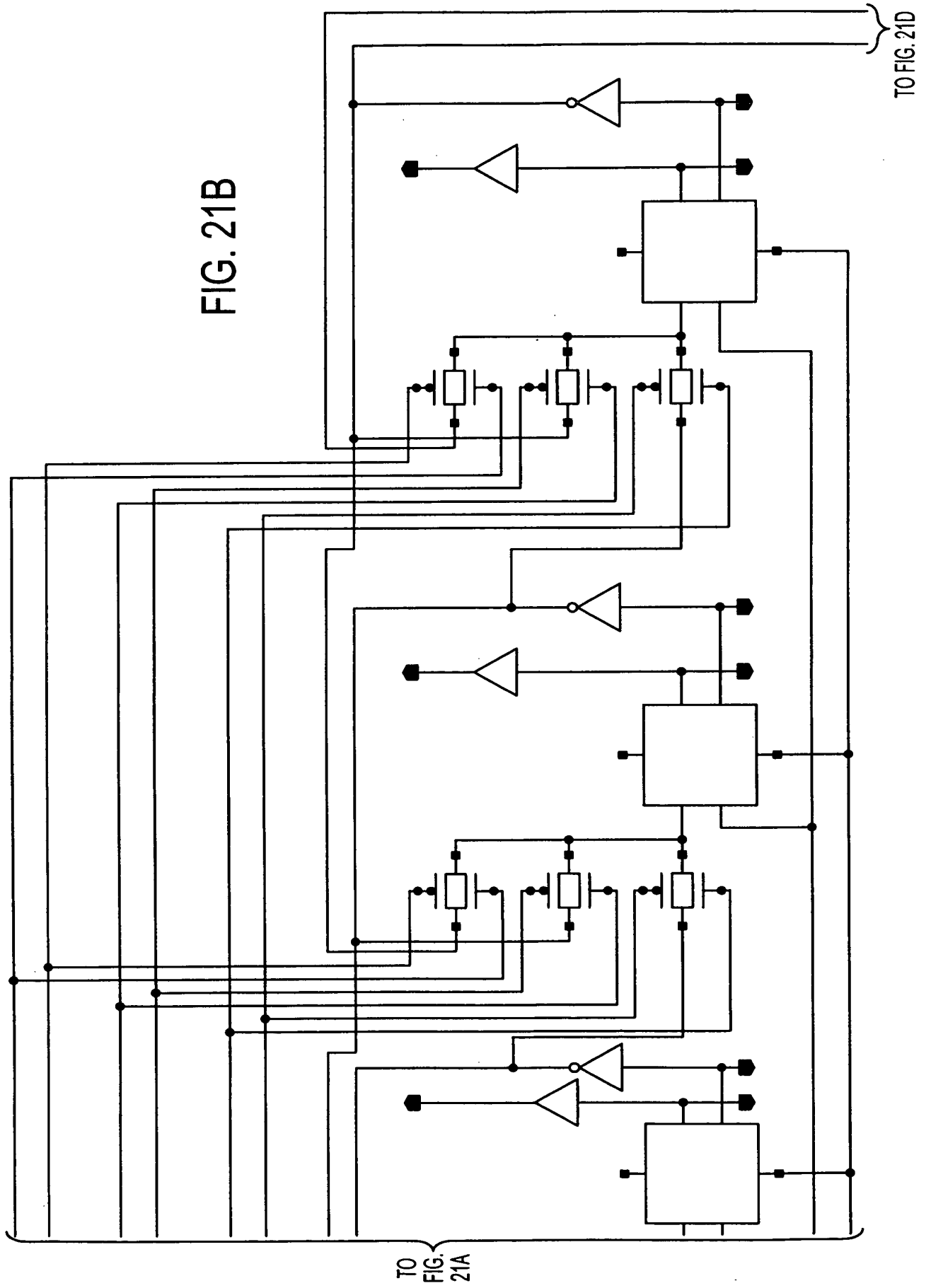




FIG. 21B



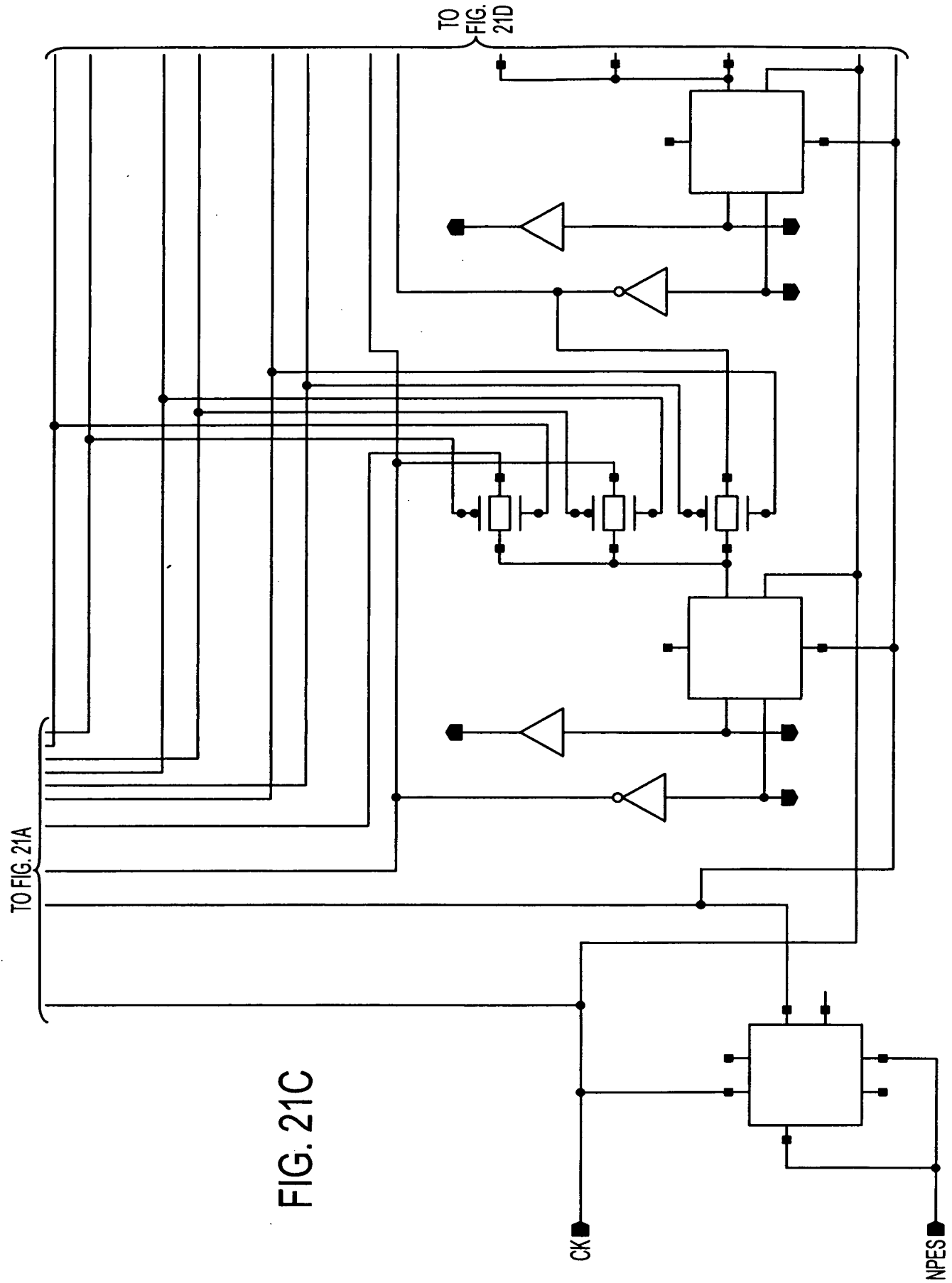


FIG. 21C

TO FIG. 21B

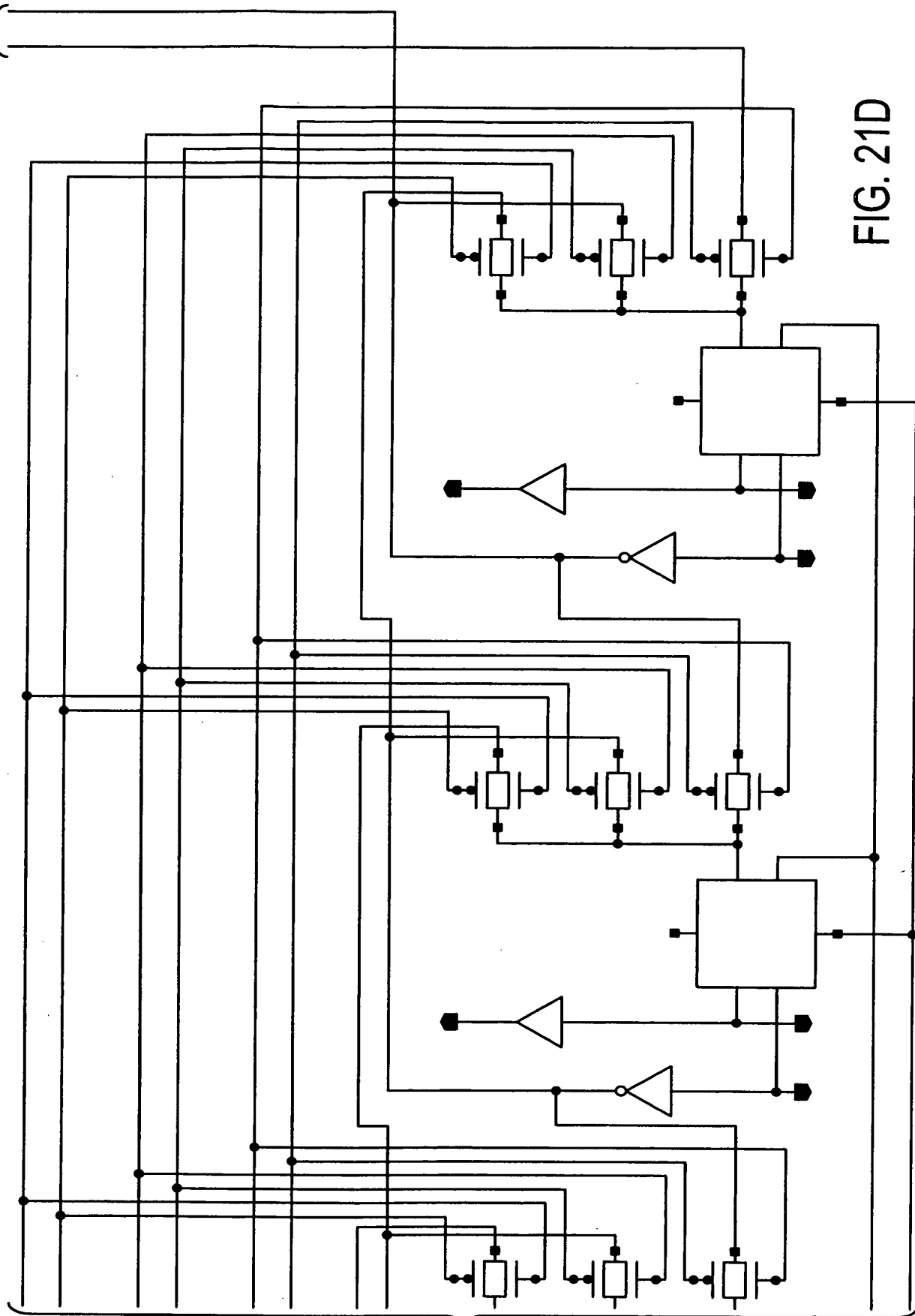


FIG. 21D

TO FIG. 21C

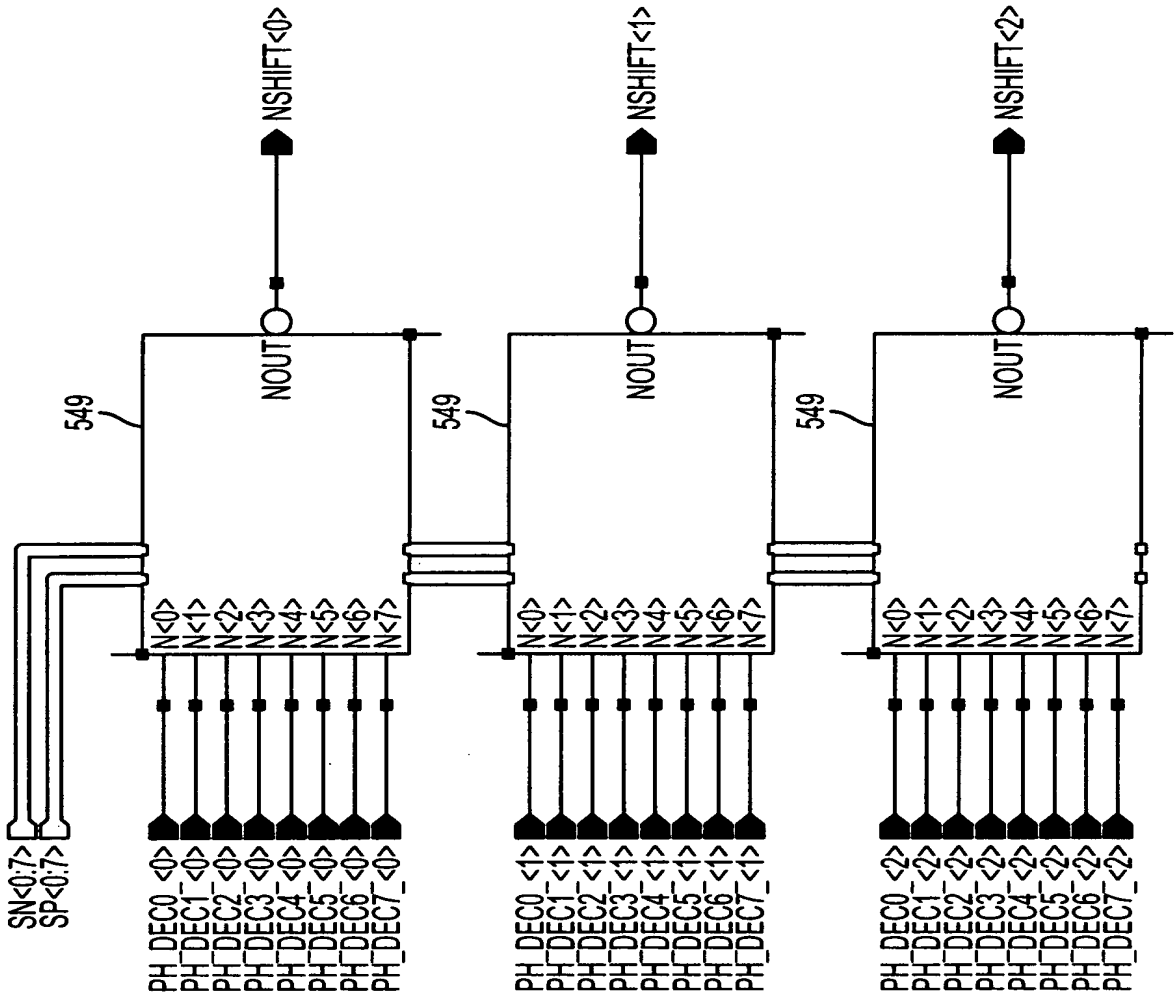


FIG. 22

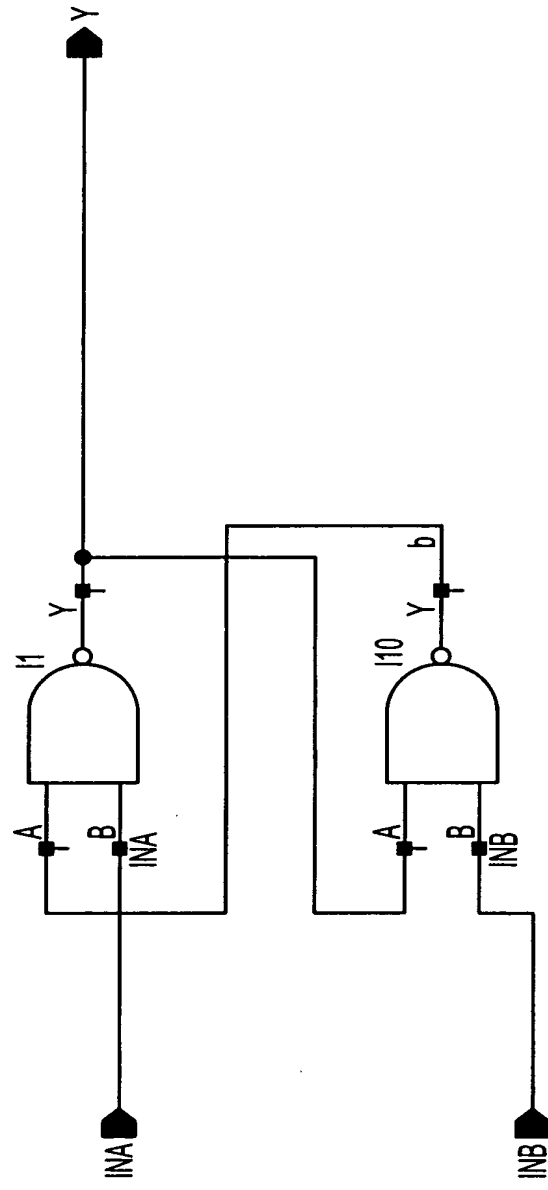


FIG. 23

FIG. 24A

ONLY ONE OF EIGHT  
NODES SHOULD BE H.

FIG. 24B

TO FIG. 24B

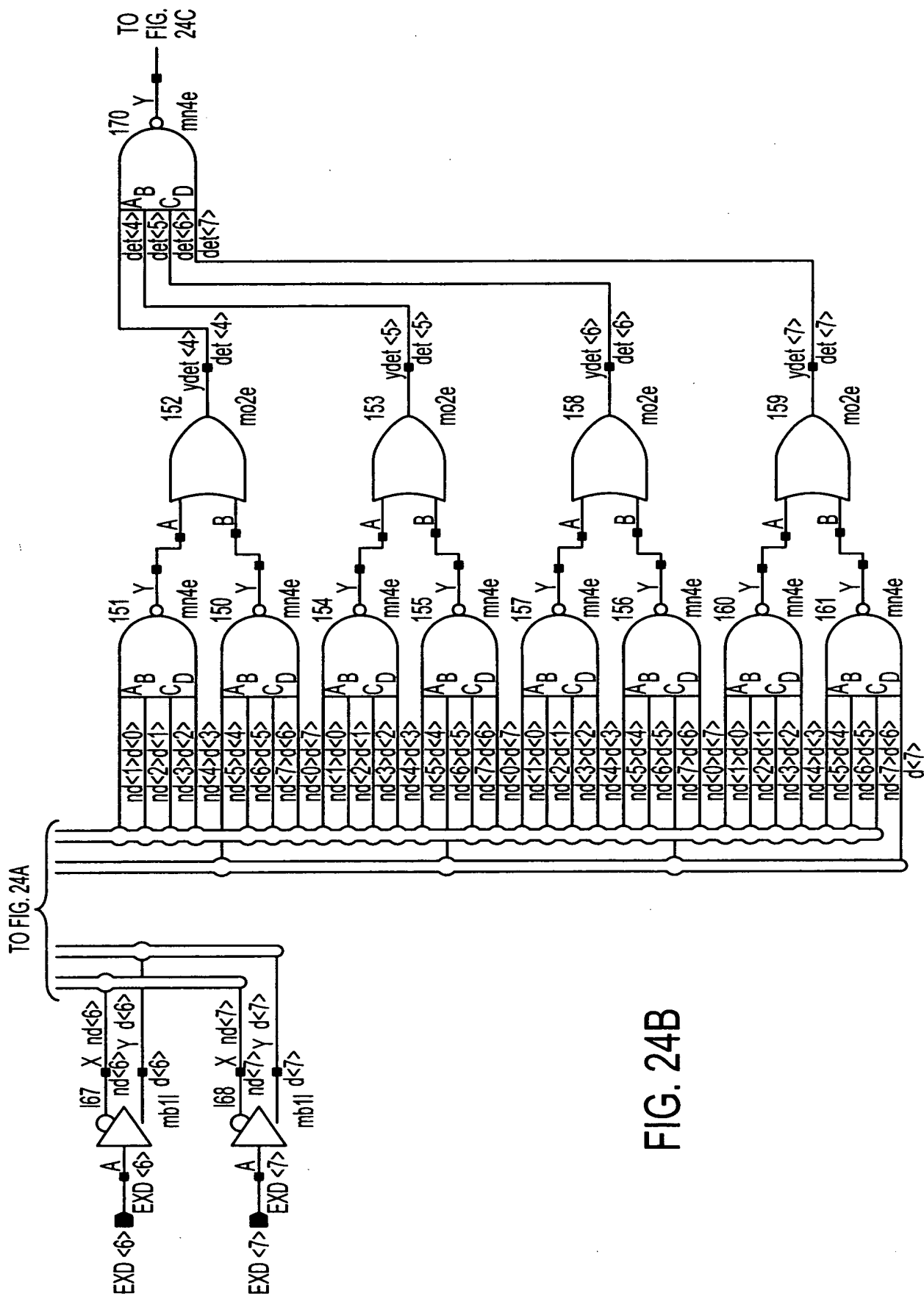


FIG. 24B

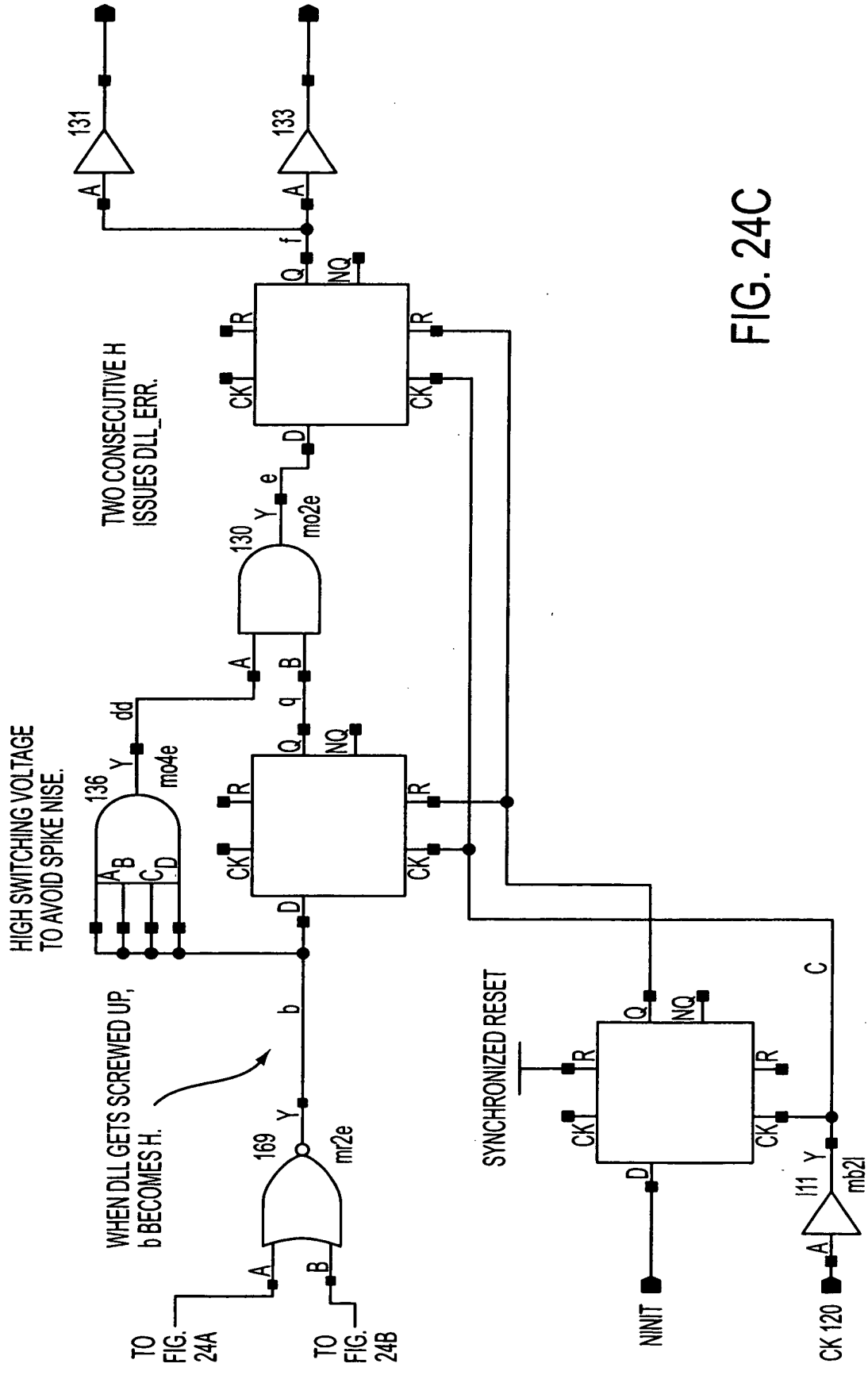


FIG. 24C



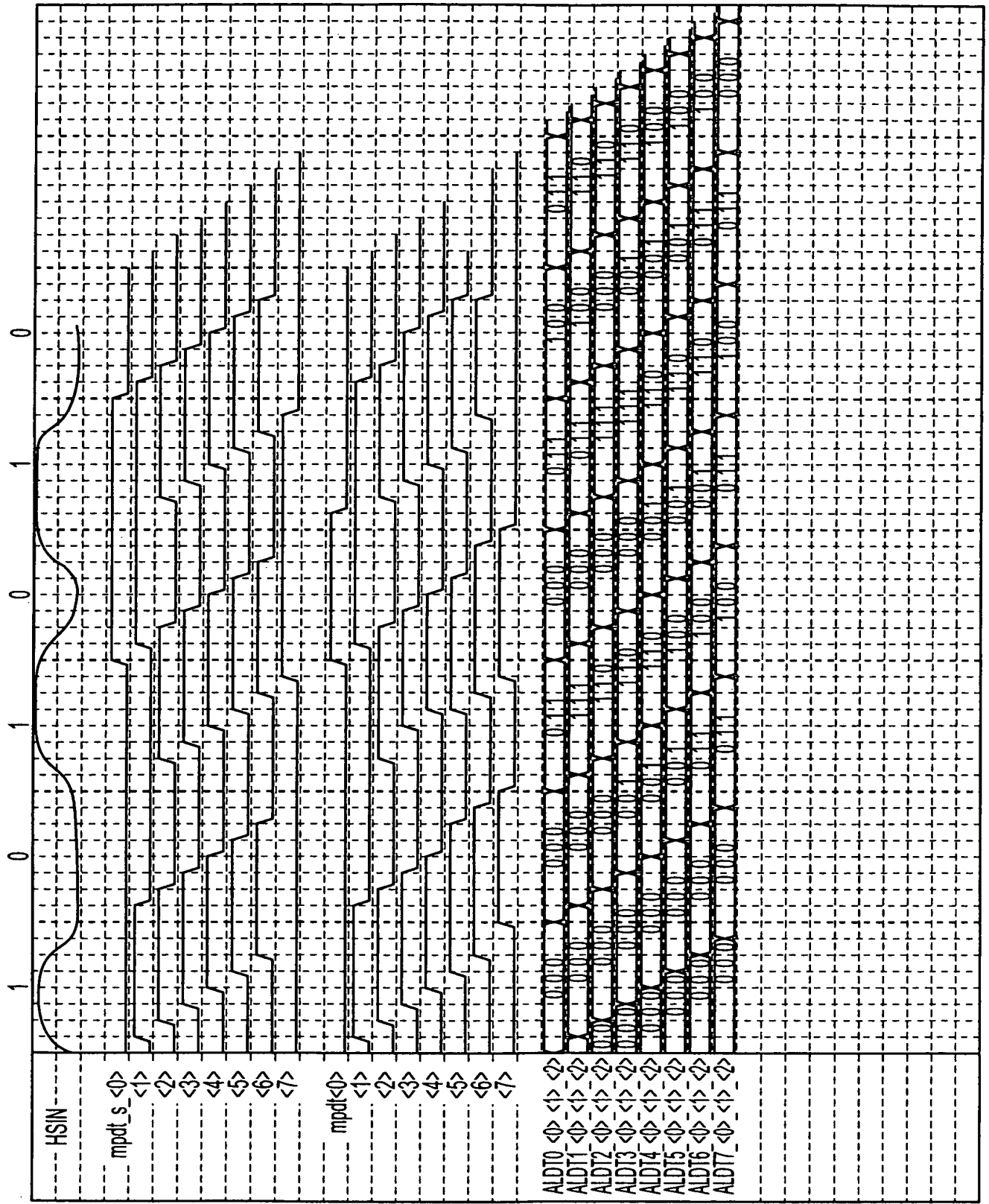


FIG.  
25A

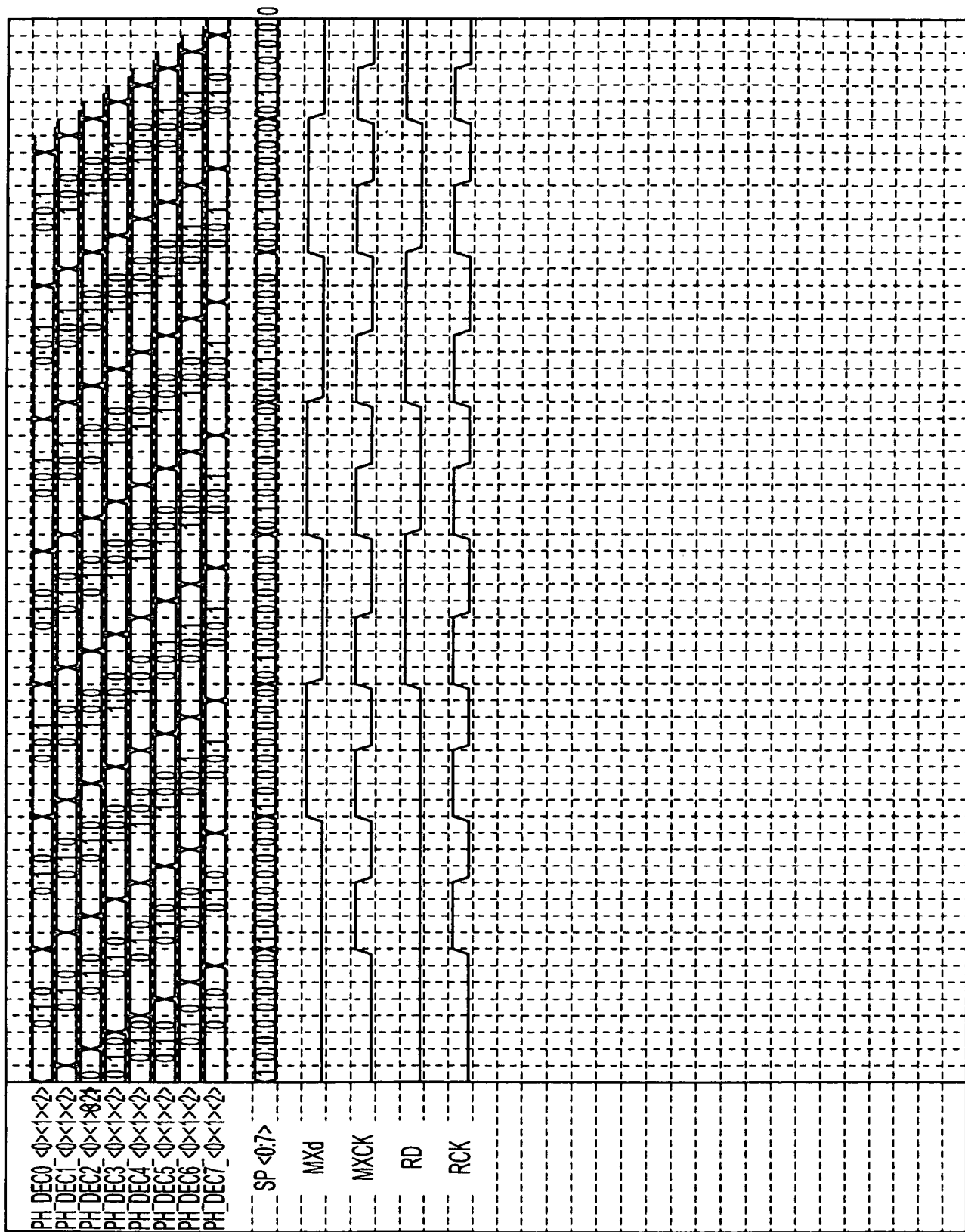


FIG.  
25B

KOUSSYDPLLMINP

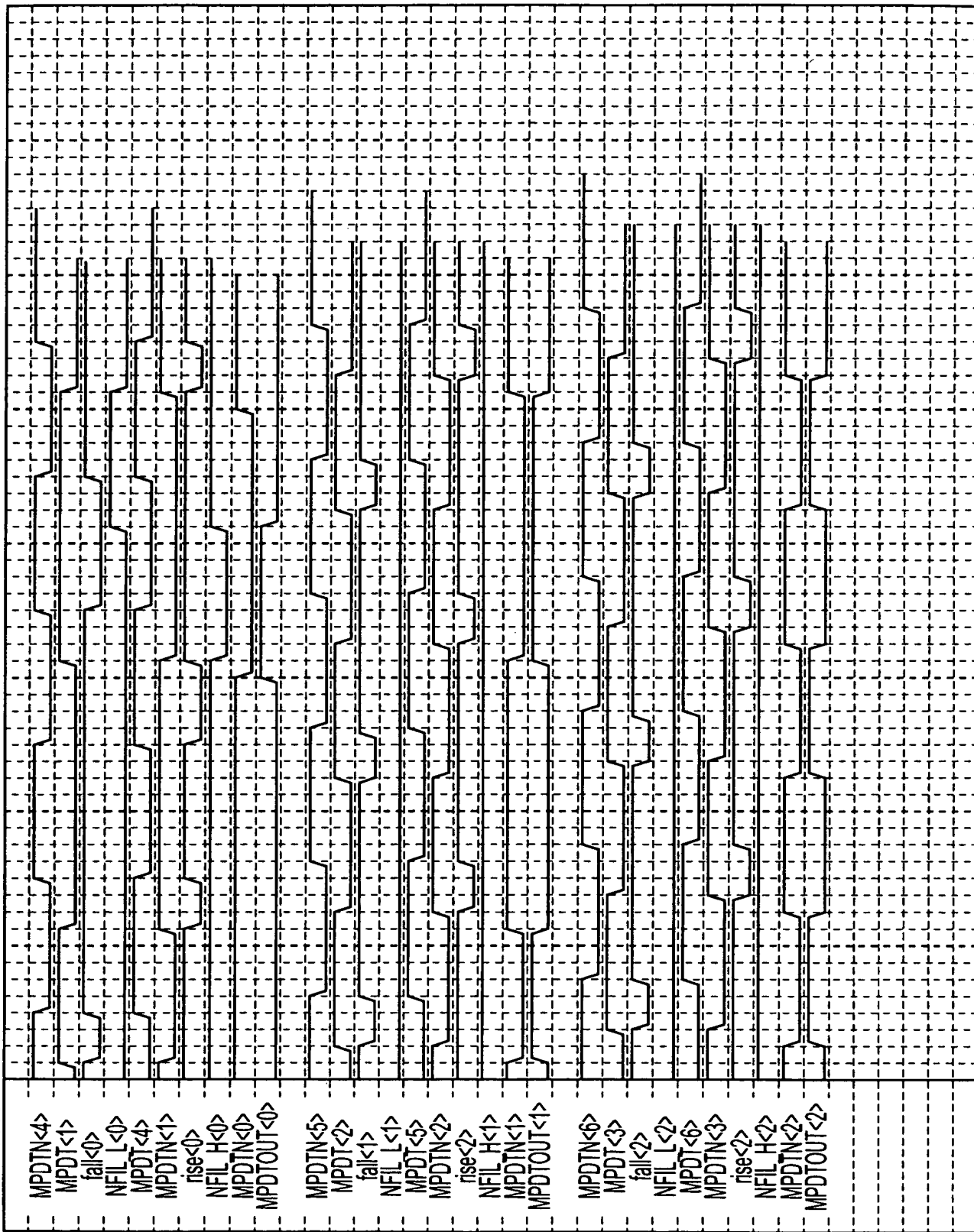


FIG.  
25C

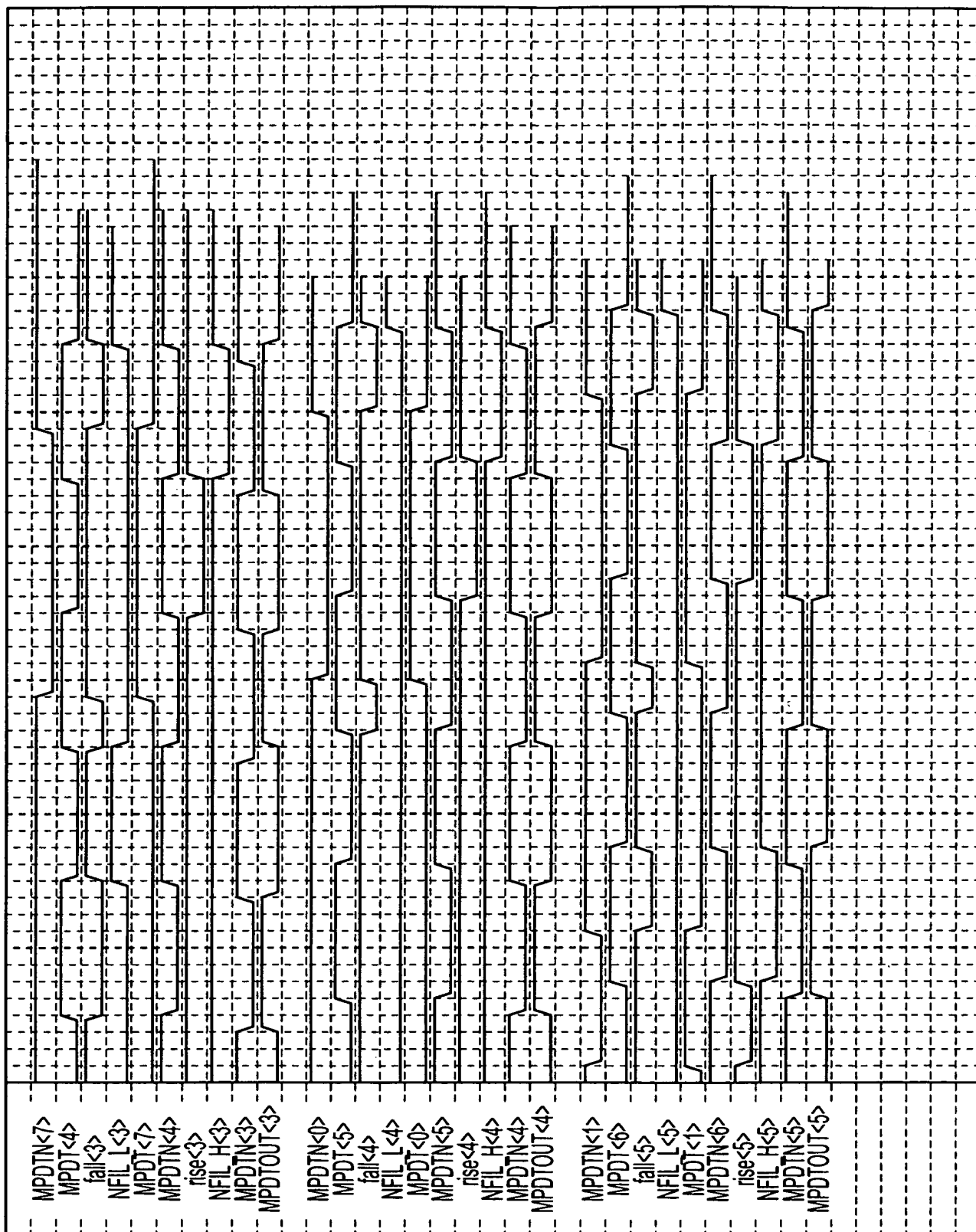


FIG.  
25D

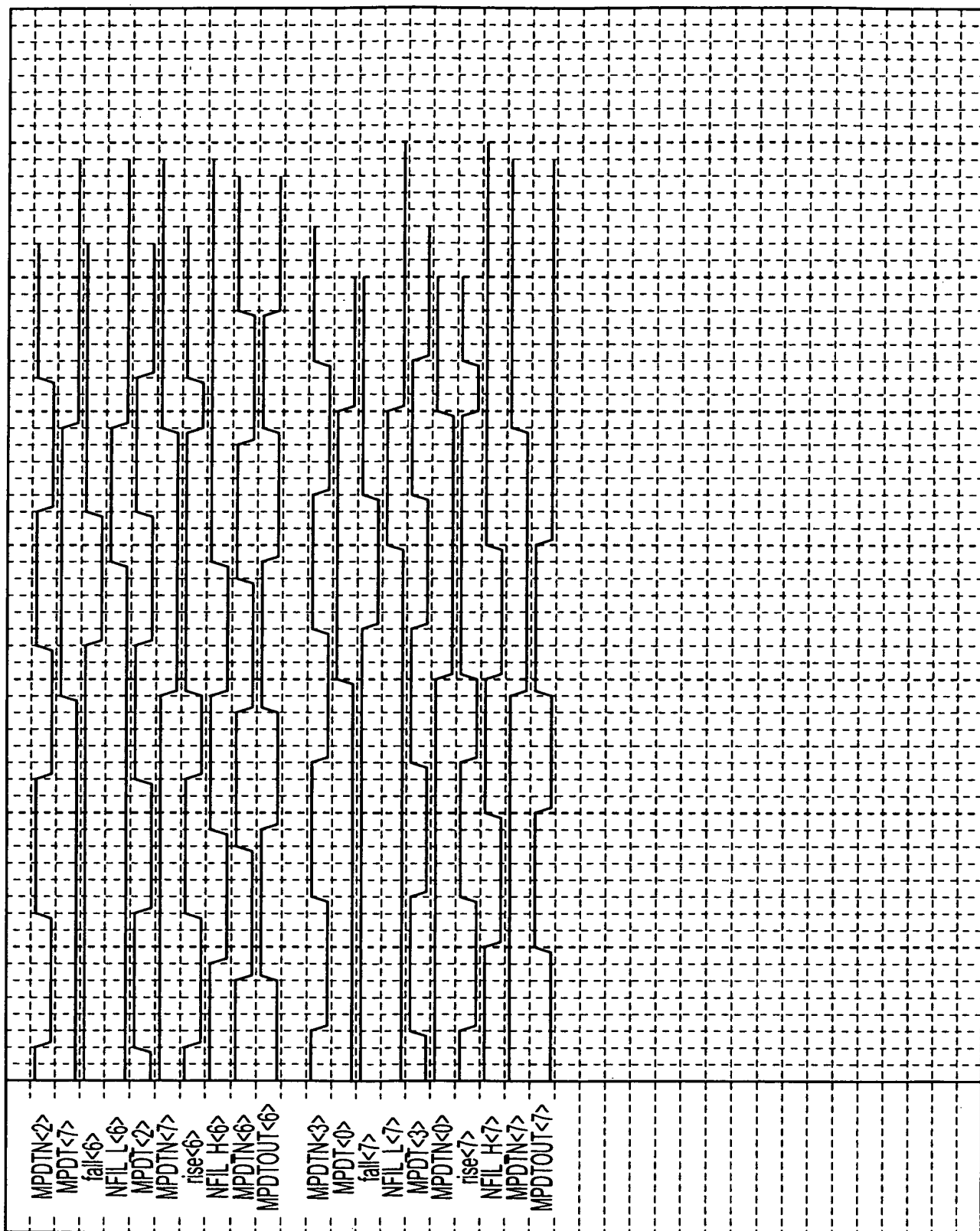


FIG.  
25E

KOUSSYDPLLAIN

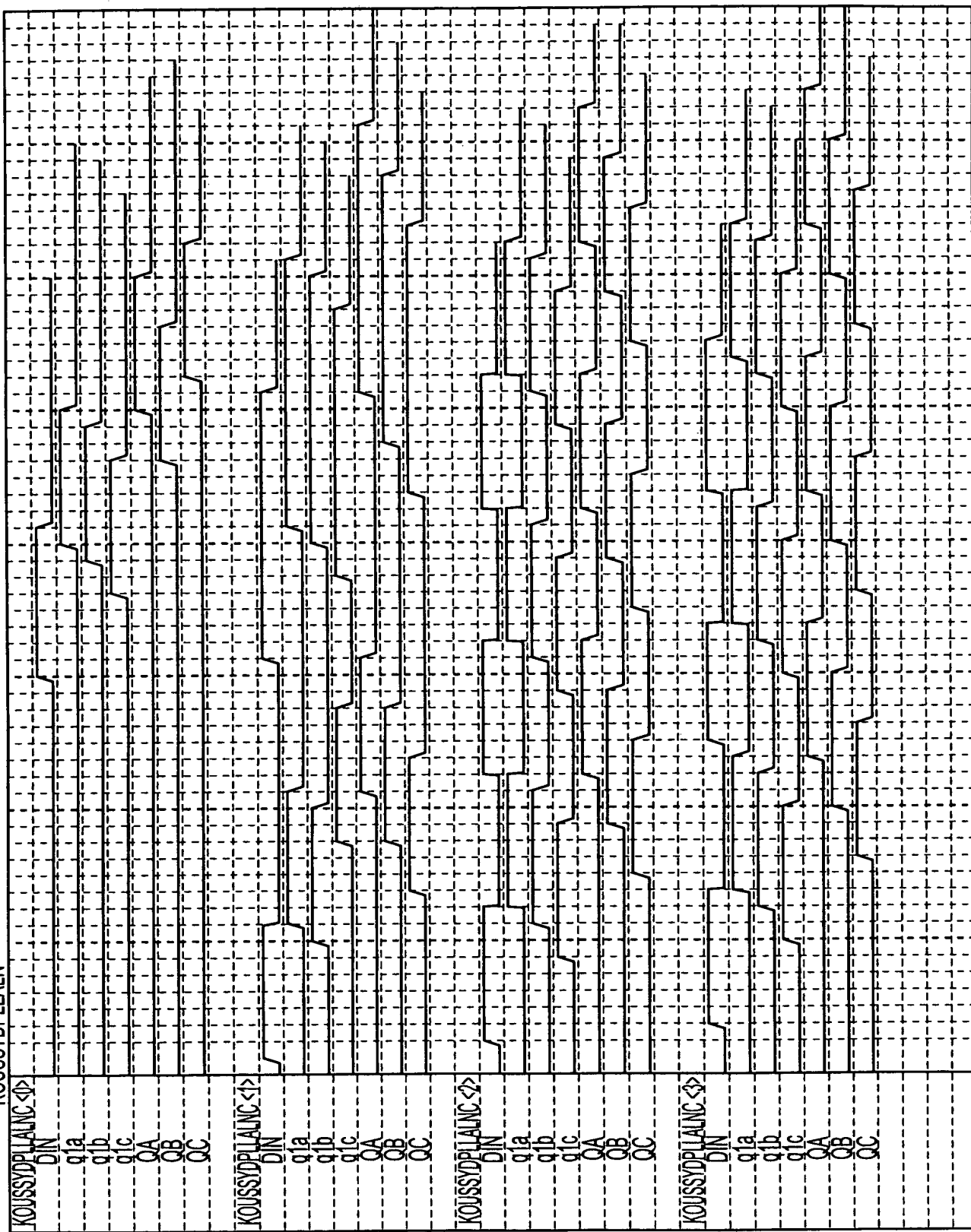


FIG.  
25F

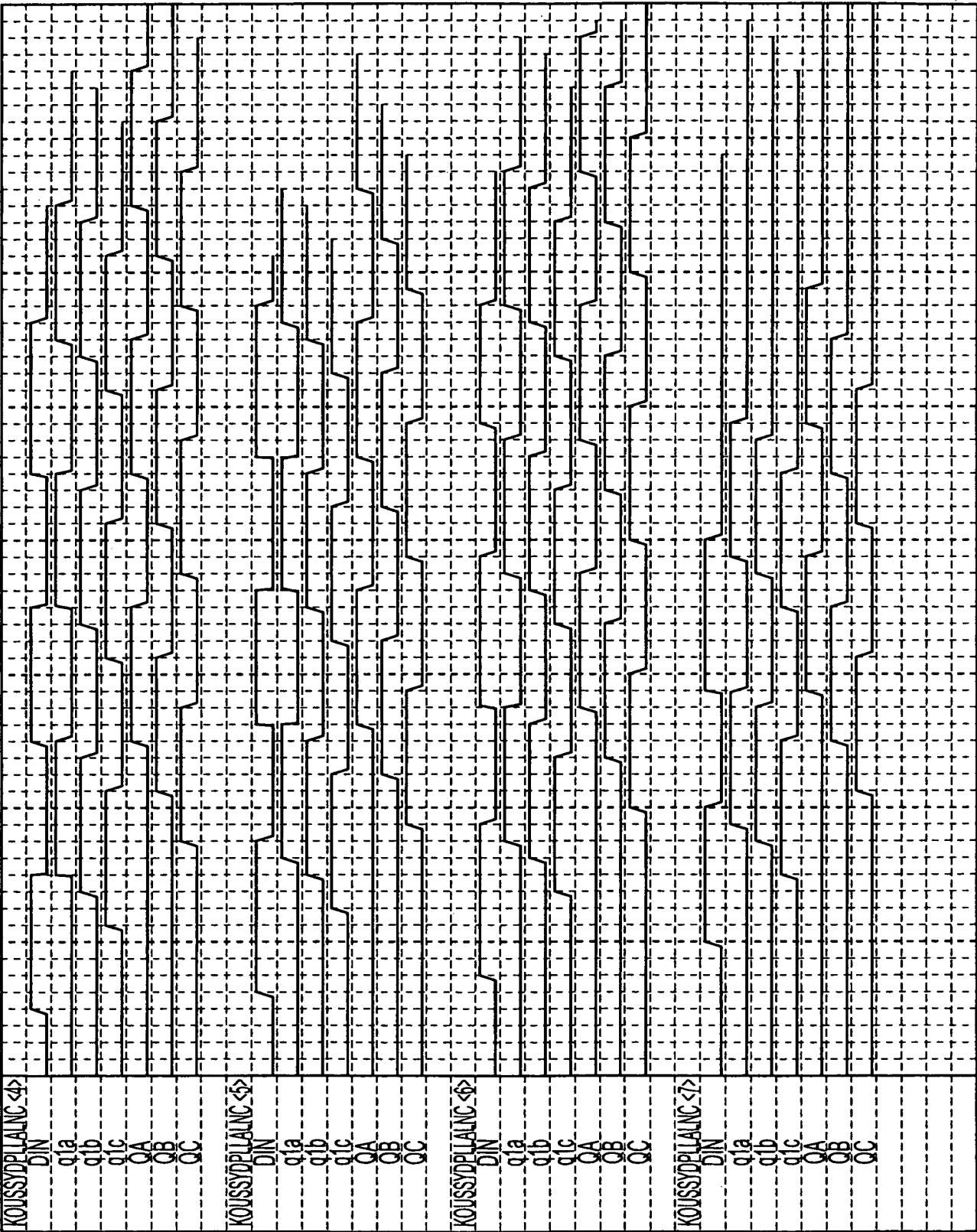


FIG.  
25G

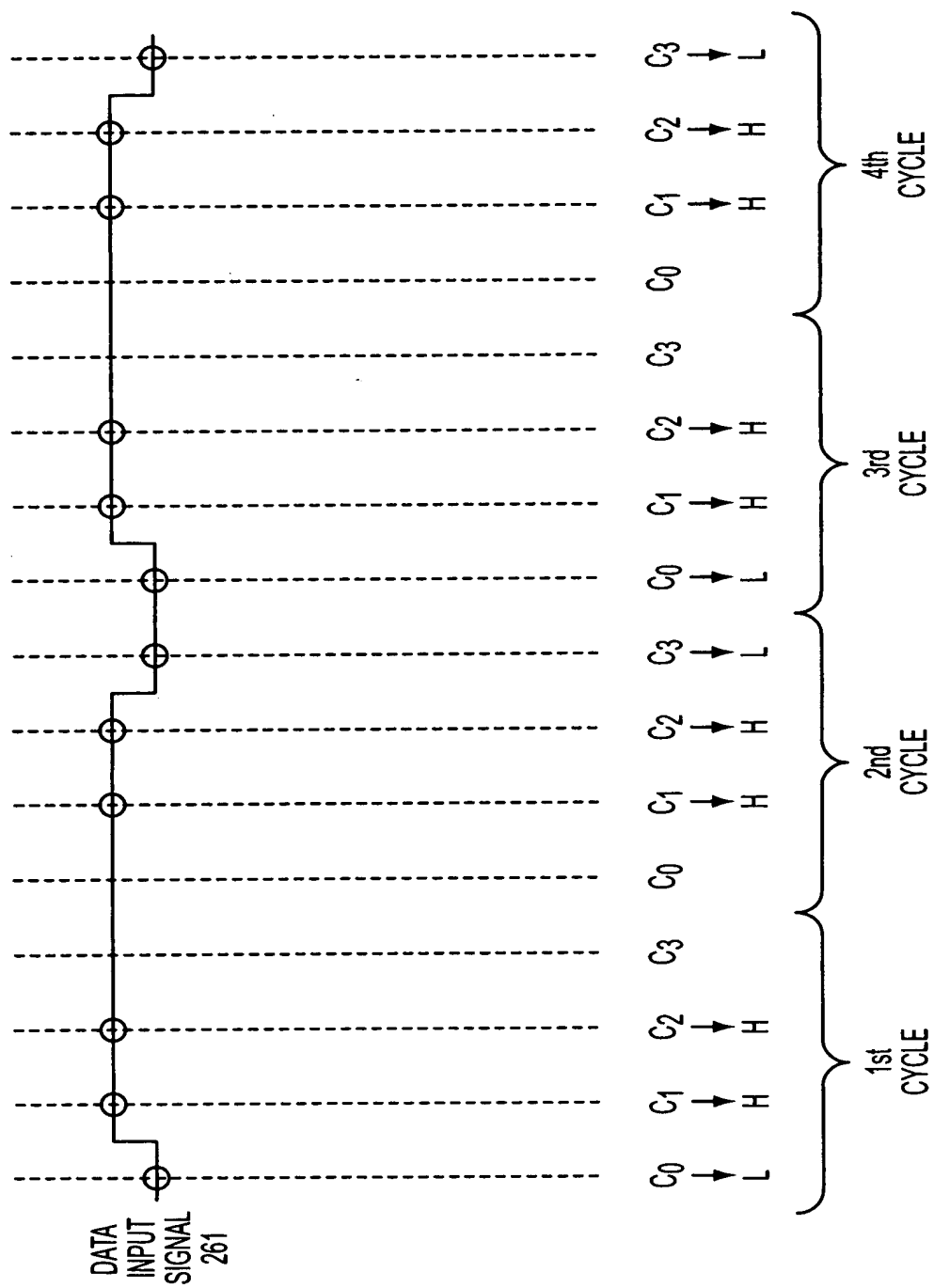


FIG. 26